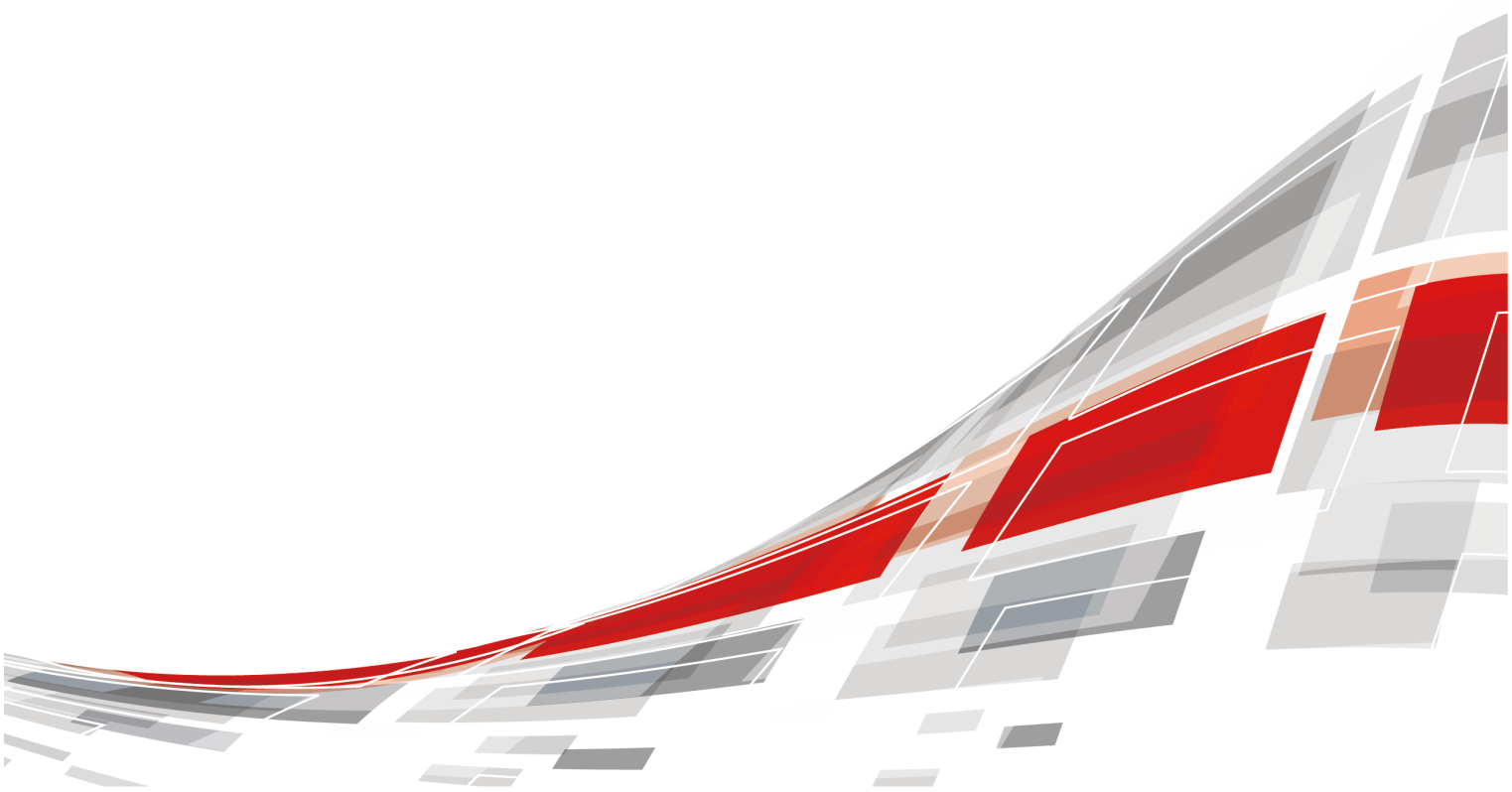
PAC1500S12-B1 PSU

Technical Manual

Issue 02

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About This Document

Purpose

This document describes the PAC1500S12-B1 and PAC1500S12-BE (collectively referred to as PAC1500S12-B*) power supply units (PSUs), including the functions and applications of their pins, as well as their heat dissipation and storage requirements.

The figures provided in this document are for reference only.

Intended Audience

This document is intended for pre-sales engineers.

Symbol Conventions

The symbols that may be found in this document are defined as follows:

Symbol	Description
DANGER	Indicates a hazard with a high level of risk which, if not avoided, will result in death or serious injury.
WARNING	Indicates a hazard with a medium level of risk which, if not avoided, could result in death or serious injury.
CAUTION	Indicates a hazard with a low level of risk which, if not avoided, could result in minor or moderate injury.
NOTICE	Indicates a potentially hazardous situation which, if not avoided, could result in equipment damage, data loss, performance deterioration, or unanticipated results. NOTICE is used to address practices not related to personal injury.
NOTE	Supplements the important information in the main text. NOTE is used to address information not related to personal injury, equipment damage, and environment deterioration.

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1 Safety Precautions

1.1 General Safety

1.2 Personnel Requirements

1.3 Electrical Safety

1.1 General Safety

Statement

Before installing, operating, and maintaining the device, read this document and observe all the safety instructions on the device and in this document.

The "NOTICE", "CAUTION", "WARNING", and "DANGER" statements in this document do not cover all the safety instructions. They are only supplements to the safety instructions. xFusion will not be liable for any consequence caused by the violation of general safety requirements or design, production, and usage safety standards.

Ensure that the PSU is used in environments that meet its design specifications. Otherwise, the PSU may become faulty, and the resulting malfunction, component damage, personal injuries, or property damage are not covered by the warranty.

Follow local laws and regulations when installing, operating, and maintaining the PSU. The safety instructions in this document are only supplements to local laws and regulations.

xFusion will not be liable for any consequences of the following circumstances:

- Although the device has passed security and compatibility tests, the radio frequency and magnetic fields emitted from the device may have a negative impact on the operation of other electronic devices. The device may affect normal operation of implanted medical appliances or personal medical appliances, such as pacemakers, cochlear implants, and hearing aids. If such appliances are used, consult the appliance manufacturer about the restrictions for using this device.
- Operation beyond the conditions specified in this document.

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- Installation or use in environments which are not specified in relevant international or national standards.
- Unauthorized modifications to the product or software code or removal of the product.
- Failure to follow the operation instructions and safety precautions on the product and in this document.
- Equipment damage due to force majeure, such as earthquakes, fire, and storms.
- Damage caused during transportation by the customer.
- Storage conditions that do not meet the requirements specified in this document.
- Do not drop, squeeze, or pierce the PSU. Protect the PSU from external high pressure to prevent internal short circuits and overheating.
- Do not disassemble, refit, or insert foreign objects into the PSU, or expose the PSU to water or other liquids, so as to avoid short circuits, overheating, fire, or electric shocks.
- Use the PSU at a temperature ranging from 5°C to 55°C (41°F to 131°F) and store the PSU at a temperature ranging from –40°C to +85°C (-40°F to +185°F).
- Do not expose the PSU to heat sources such as direct sunlight, radiators, microwave ovens, ovens, or water heaters.
- If the PSU is damaged, cracked, or wet, stop using it. Continuing to use it in such conditions may lead to electric shocks, short circuits, or fire.
- Dispose of the PSU in accordance with local regulations. Do not dispose of it as normal household waste. Follow local regulations on disposal of the PSU and its accessories, and support proper collection and recycling.
- Keep the PSU dry. Do not use the PSU in a dusty or damp place. Otherwise, the PSU may be faulty. Do not splash water on the PSU. Keep the PSU far away from fire sources. Do not ignite the PSU.

WARNING

In a household scenario, operation of this device may cause wireless interference.

Personal Safety

- Do not alter, disassemble, or remove the shell of the PSU.
- If there is a probability of personal injury or PSU damage during operations on the PSU, immediately stop the operations, report the case to the supervisor, and take feasible protective measures.
- Before the installation is completed or confirmed by professionals, do not power on the PSU.

1.2 Personnel Requirements

- Personnel who plan to install, operate or maintain the PSU must receive thorough training, understand all necessary safety precautions, and be able to correctly perform all operations.
- During the installation, operation, and maintenance of the PSU, no collision or drop is allowed.

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- During the secondary assembly of the PSU, do not introduce conductive foreign objects.

1.3 Electrical Safety

Operation Requirements

WARNING

Failure to follow the operating procedures may cause personal injury or even death.

- Only qualified personnel are allowed to perform operations to prevent electric shocks.
- Before installing or removing the PSU, disconnect the upstream power supply to the PSU.
- Before powering on the PSU, ensure that the PSU cables are correctly connected.
- Before making electrical connections, disconnect the upstream power supply to the PSU if people may contact energized components.
- Do not open, alter, or repair the product as the voltage inside the product is high.
- To ensure safety, connect the shell ground and the device ground reliably. When installing the device, install the PGND cable first. When uninstalling the device, remove the PGND cable last.
- Do not operate the PSU with wet hands. Otherwise, electric shocks may occur.
- The voltage between the L or N terminal of the PSU and the PE terminal cannot exceed 318 V AC.
- If any damage or fault occurs during installation or operation, power off the PSU immediately and return it to the manufacturer for inspection or repair.
- In case of emergency, remove the power plug from the power outlet to completely cut off the power supply.
- If the PSU has multiple inputs, disconnect all the inputs to completely power it off before operating the PSU.

ESD

- When installing, operating, and maintaining the PSU, comply with the ESD protection regulations and wear the ESD clothing, gloves, and wrist strap.
- When holding the PSU, hold its edge without touching any components. Do not touch the components with your bare hands.
- Package the removed PSU with ESD packaging materials before storing or transporting it.

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2 Product Overview

The PAC1500S12-B* supports AC input of 90 V to 264 V and HVDC input of 180 V to 300 V. The PSU provides one output and supports three output modes (MV12, SV12 and MV6). The rated output power is 1500 W.

The hot swappable PSU supports current sharing, 1+1/2+2/3+3 parallel usage, and N +R redundancy.

The PSU provides an I2C communication function for reporting the vendor, model, version and other information. It provides a black box and supports online upgrade. The PSU is designed according to the safety requirements and complies with the information technology equipment safety standards.

Model Naming Convention

$$\frac{P}{1}\ \frac{AC}{2}\ \frac{1500}{3}\ \frac{S}{4}\ \frac{12}{5}\ -\ \frac{B}{6}\ \frac{*}{7}$$

No.	Description	No.	Description
1	Embedded power	2	AC input
3	Output power: 1500 W	4	Single output
5	Output voltage: 12 V DC	6	Platinum energy efficiency certification
7	SN	-	-

Features

- Efficiency: ≥ 94% (T_A = 25°C or 77°F; V_{in} = 230 V AC; P_{out} = 750 W; without a fan).
- Depth x Width x Height: 183.0 mm x 68.0 mm x 40.3 mm (7.20 in. x 2.68 in. x 1.59 in.)
- Weight: < 2 kg
- Supports input protection for overvoltage and undervoltage. Supports output protection for overvoltage, overcurrent, short circuit, and overtemperature.

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- I2C communication for control, online upgrade, and monitoring.
- AC: UL, TUV, CB, CE, CCC, BSMI, and BIS certification available.
- 240 HDVC: UL, TUV, CCC, CB, CE, BSMI, and BIS certification available.
- 80 PLUS platinum energy efficiency certification.
- IEC 60950-1, EN 60950-1, UL 60950-1, IEC 62368-1, GB 4943.1
- RoHS6 compliant
- 2002/95/EC compliant

Application Scenario

Servers

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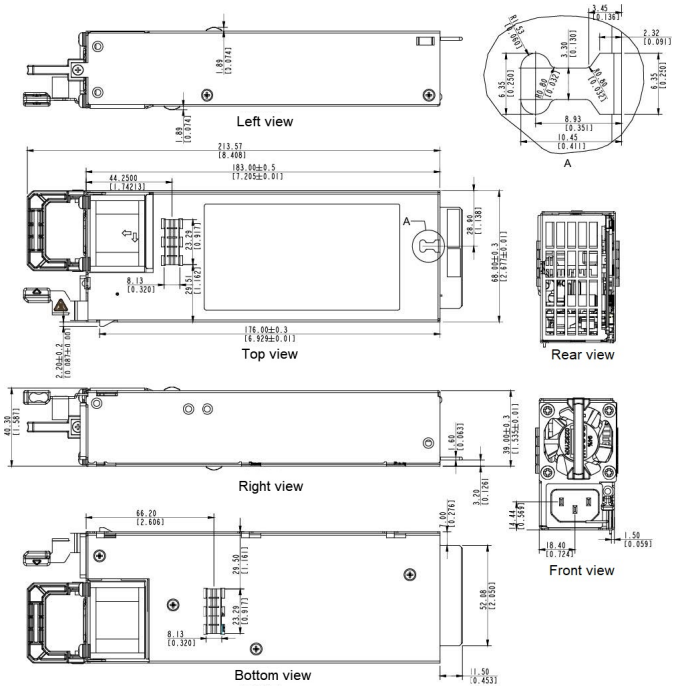
3 Product Structure Overview

3.1 Structural Dimensions

3.2 External Ports

3.1 Structural Dimensions

Depth x Width x Height: 183.0 mm × 68.0 mm × 40.3 mm (7.20 in. × 2.68 in. × 1.59 in.)



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3.2 External Ports

Figure 3-1 Output port (top view)

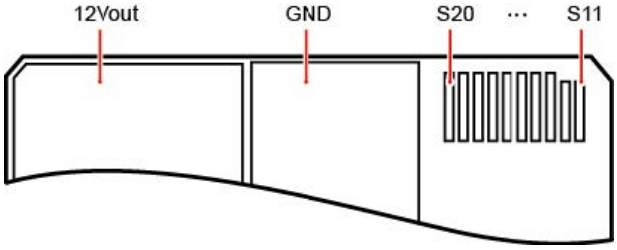
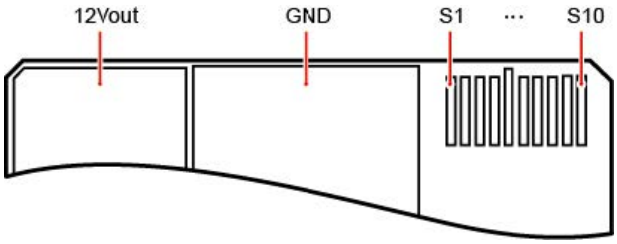


Figure 3-2 Output port (bottom view)



Pin	Name	Description
P1-P6, P21-P28	V12	PSU 12 V main output
P7-P20	RTN	PSU output GND
S20	OPOK	Signal for indicating whether the main output is normal
S19	IPOK	Signal for indicating whether the input is normal
S18	IPOK Link	Signal for indicating whether the input of the redundant PSU is normal
S17	SMART_ON	Signal for waking up the cold standby power mode
S16	SHLFBB_SF1	Reserved
S15	INSTALLED54#	Reserved
S14	IP PRESENT	SV12 control signal
S13	PSON12V#	MV12 remote on/off control signal

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Pin	Name	Description
S12	INSTALLED#	PSU installation signal
S11	PRESENT#	PSU presence signal
S10	I-MON	Current sharing signal
S9	EFUSEV	System E-Fuse voltage
S8	CYC_PWR#	Power supply cycle power signal
S7	PS_INTERRUPT#	PSU alarm interruption signal
S6	SDA	I2C data cable
S5	I2C Sig GND	I2C signal ground
S4	SCL	I2C Serial Clock Line
S3	ADD2	Address cable 2
S2	ADD1	Address cable 1
S1	ADD0	Address cable 0

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4 Electrical Specifications

4.1 Environmental Characteristics
4.2 Input Characteristics
4.3 Output Characteristics
4.4 Efficiency Characteristics
4.5 Protection Characteristics

4.1 Environmental Characteristics

Parameter	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
Operating temperature (T_A)	5	25	55	°C	• The PSU is able to start at –10°C (14°F), but there are no requirements on the PSU performance. • When the temperature is 65°C (149°F), the PSU can run reliably at half load.
Storage temperature	–40	25	85	°C	-
Relative humidity	5	-	95	% RH	Non-condensing. The PSU can work properly.
Altitude	–60	-	5000	m	5000 m (16,404 ft) in CCC certification. When the PSU is not working, it can be placed at an altitude of 15,000 m (49,212 ft).

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Parameter	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
Atmospheric pressure	61	-	106	kPa	● The PSU can run normally with the atmospheric pressure at an altitude of 4000 m (13,123.36 ft). ● Passed the low atmospheric pressure test at an altitude of 4000 m (13,123.36 ft) and with rated input of 230 V AC. ● When the altitude ranges from 1800 m to 4000 m (5905.51 ft to 13,123.36 ft), the highest operating temperature decreases by 1°C (1.8°F) for every increase of 220 m (721.78 ft).

4.2 Input Characteristics

Parameter	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
AC input voltage (V_{in})	90	-	264	V	Supports single-phase three-wire input, that is, 110 V AC/220 V AC single-phase input and 110 V AC dual-live-wire input.
Rated AC input voltage	100	230	240	V	-
AC input frequency	47	50/60	63	Hz	-
HVDC input voltage	180	240	300	V	The PSU can still work normally even in the case of reverse polarity wiring.
THDi (MV12 output mode)	-	-	-	%	No requirement at below 10% load.
	-	-	20	%	T_A = 25°C (77°F), V_{in} = 208 V AC (60 Hz) or 230 V AC (50 Hz), and 10% load
	-	-	10	%	T_A = 25°C (77°F), V_{in} = 208 V AC (60 Hz) or 230 V AC (50 Hz), and 20% load

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Parameter	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
	-	-	5	%	T_A = 25°C (77°F), V_{in} = 208 V AC (60 Hz) or 230 V AC (50 Hz), and ≥ 30% load
THDv	-	-	10	%	THDv ≤ 10%: The PSU can work properly.
Power factor (MV12)	0.94	-	-	-	T_A = 25°C (77°F), V_{in} = 208 V AC (60 Hz), 230 V AC (50 Hz), or 240 V AC (60 Hz), and 10% load
	0.96	-	-	-	T_A = 25°C (77°F), V_{in} = 208 V AC (60 Hz), 230 V AC (50 Hz), or 240 V AC (60 Hz), and 20% load
	0.98	-	-	-	T_A = 25°C (77°F), V_{in} = 208 V AC (60 Hz), 230 V AC (50 Hz), or 240 V AC (60 Hz), and 50% load
	0.99	-	-	-	T_A = 25°C (77°F), V_{in} = 208 V AC (60 Hz), 230 V AC (50 Hz), or 240 V AC (60 Hz), and 100% load
Maximum AC input current (RMS)	-	-	10	A	V_{in} = 200 V AC, rated load output
HVDC input current	-	-	8	A	V_{in} = 240 V DC, rated load output
AC input DC bias	-	-	50	mA	Rated input, full-range output
Input inrush current	-	-	30	A	ETSI 300 132-3 and ETSI EN 300 132-3 compliant
Standby power consumption	-	-	1	W	• T_A = 25°C (77°F), V_{in} = 115 V AC/230 V AC/240 V DC. • The fan, output and PFC are shut down. The PSU works in deep sleep mode (in parallel mode).

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Parameter	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
	-	-	8	W	• T_A = 25°C (77°F), V_{in} = 115 V AC/230 V AC/240 V DC. • The fan works at the lowest rotational speed, and the output is 0 A. • The standby PSU works in cold standby and active/standby modes. • The standby PSU works without any load.
Harmonic current	-	-	-	-	Meets the test requirements of class A equipment (T_A = 25°C or 77°F).
C32 trip prevention characteristic	-	-	-	-	If an internal fault occurs in the PSU, the upstream C32 circuit breaker is not allowed to trip.

4.3 Output Characteristics

Parameter	Output	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
Number of outputs	-	-	-	Single route	-	There is a single output. The output mode can be MV12 (V_{out} = 12 V DC), SV12 (V_{out} = 12 V DC) or MV6 (V_{out} = 7.6 V DC).
Output power	MV12	-	-	1500	W	V_{in} = 176 V AC–264 V AC or V_{in} = 180 V–300 V DC.
		-	-	1000	W	V_{in} = 100 V AC–176 V AC.
		-	-	900	W	V_{in} = 90 V AC–100 V AC.
	SV12	-	-	540	W	-

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Parameter	Output	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
	MV6	-	-	60	W	-
Output voltage setpoint	MV12/SV12	12.27	12.30	12.33	V	T_A = 25°C (77°F), rated input, I_{out} = 3 A
	MV6	7.3	7.6	7.9	V	T_A = 25°C (77°F), rated input, I_{out} = 3 A
Output voltage	MV12/SV12	11.70	-	12.60	V	Full-load range.
	MV6	6.4	7.6	8.2	V	
Output current	MV12	3	-	125	A	V_{in} = 176 V AC–264 V AC or V_{in} = 180–300 V DC, maximum power ≤ 1500 W
		3	-	83.3	A	V_{in} = 100 V AC–176 V AC, maximum power ≤ 1000 W
		3	-	75	A	V_{in} = 90 V AC–100 V AC, maximum power ≤ 900 W
	SV12	1	-	45	A	-
	MV6	1	-	10	A	-
Source regulation rate	-	–1	-	1	%	-
Ripple and noise (peak to peak)	MV12	-	-	120	mV	● Before the test, add a minimum load of 3 A at the output end, connect a 10 µF capacitor and a 0.1 µF ceramic capacitor in parallel, and set the oscilloscope bandwidth to 20 MHz. (Full range of temperature and full input voltage). ● Output power < 1000 W, tested with the minimum capacitive load 540 µF. Output power > 1000 W, tested with the minimum capacitive load 1000 µF.

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Parameter	Output	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
	SV12	-	-	360	mV	Before the test, connect a 10 μF capacitor and a 0.1 μF ceramic capacitor in parallel at the output end, and set the oscilloscope bandwidth to 20 MHz. Add a minimum load of 1 A and tested with the minimum capacitive load 270 μF.
	MV6	-	-	360	mV	Before the test, connect a 10 μF capacitor and a 0.1 μF ceramic capacitor in parallel at the output end, and set the oscilloscope bandwidth to 20 MHz. Add a minimum load of 1 A and tested with the minimum capacitive load.
Dynamic	MV12	11.6	-	12.6	V	Current change rate: 0.5 A/μs, T = 10 ms (5 ms of CCDHT 1 and 5 ms of CCDHT 2); load: 25%–50%–25% or 50%–75%–50% Tested with the minimum capacitive load.
		11.4	-	12.8	V	Current change rate: 0.5 A/μs, T = 10 ms (5 ms of CCDHT 1 and 5 ms of CCDHT 2); load: 50%–100%–50% The bus voltage should not be below 11 V and the time of the voltage below 11.4 V should not exceed 100 μs.
	SV12	11.4	-	12.6	V	Current change rate: 0.5 A/μs, T = 10 ms (5 ms of CCDHT 1 and 5 ms of CCDHT 2); load: 25%–50%–25% or 50%– 75%–50%. Tested with the minimum capacitive load.
	MV6	6.0	-	8.2	V	Current change rate: 0.5 A/μs, T = 10 ms; load: 5%–100%. Tested with the minimum capacitive load.
Transient impact power overshoot	MV12/SV12	11.4	-	12.8	V	● Current change rate: 0.5 A/μs, T = 1 s–10 ms–1 s; load: 65%–130%–65% ● The bus voltage must be greater than or equal to 11 V and the time when the voltage is below 11.4 V should not exceed 100 μs.

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Parameter	Output	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
Overshoot at power-on/off	MV12/SV12	–5	-	5	%	The PSU is able to start at –10°C, but there are no requirements on the PSU performance.
Temperature coefficient	-	–0.02	-	0.02	%/°C	Rated output voltage, rated output current and full range of T_A.
Current share error	MV12	–5	-	5	%	50%–100% load (rated load of a single PSU in parallel mode)
		–10	-	10	%	200 W–50% load (rated load of a single PSU in parallel mode)
		-	-	-	-	The load of a single PSU is less than 200 W (value in this case not tested).
Capacitive load	MV12	540	-	22,000	μF	Full voltage, full load and full range of temperature (≤ 1000 W)
		1000	-	22,000	μF	Full voltage, full load and full range of temperature (> 1000 W)
	SV12	270	-	1000	μF	-
	MV6	270	-	1000	μF	
Hot swap	MV12	11.4	-	12.6	V	The backplane voltage cannot exceed the dynamic requirements for the PSU during the swap. In MV12/SV12 mode, the bus voltage must be greater than or equal to 11.4 V. The bus voltage must be greater than or equal to 11 V and the time when the voltage is below 11.4 V should not exceed 100 μs.
	MV6	6.0	-	8.2	V	

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4.4 Efficiency Characteristics

Parameter	Output	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
Efficiency	MV12	87	-	-	%	T_A = 25°C (77°F), V_{in} = 230 V AC, P_{out} = 150 W, without a fan
		91	-	-	%	T_A = 25°C (77°F), V_{in} = 230 V AC, P_{out} = 300 W, without a fan
		94	-	-	%	T_A = 25°C (77°F), V_{in} = 230 V AC, P_{out} = 750 W, without a fan
		91	-	-	%	T_A = 25°C (77°F), V_{in} = 230 V AC, P_{out} = 1500 W, without a fan
		87	-	-	%	T_A = 25°C (77°F), V_{in} = 115 V AC/V_{in} = 240V DC, P_{out} = 300 W, without a fan
		91	-	-	%	T_A = 25°C (77°F), V_{in} = 115 V AC/V_{in} = 240V DC, P_{out} = 500 W, without a fan
		89	-	-	%	T_A = 25°C (77°F), V_{in} = 115 V AC/V_{in} = 240V DC, P_{out} = 1000 W, without a fan
	MV6	70	-	-	%	T_A = 25°C (77°F), V_{in} = 230 V AC/V_{in} = 240 V DC, I_{out} = 8 A, without a fan

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4.5 Protection Characteristics

Table 4-1 Input Protection

Parameter	Output	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
AC input overvoltage protection threshold	-	280	-	-	V	Self-recovery
AC input overvoltage recovery threshold	-	275	-	-	V	Hysteresis ≥ 5 V
AC input undervoltage protection threshold	-	-	-	85	V	Self-recovery
AC input undervoltage recovery threshold	-	-	-	90	V	Hysteresis ≥ 5 V (During recovery, the PSU cannot start in the hysteresis range.)

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Parameter	Output	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
AC input overcurrent or short circuit protection	-	-	-	-	-	Use a fast-blow fuse.
HVDC input overvoltage protection threshold	-	310	-	-	V	Self-recovery
HVDC input overvoltage recovery threshold	-	305	-	-	V	Hysteresis ≥ 5 V
HVDC input undervoltage protection threshold	-	-	-	175	V	Self-recovery

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Parameter	Output	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
HVDC input undervoltage recovery threshold	-	-	-	180	V	Hysteresis ≥ 5 V
PFC overvoltage protection	-	-	-	-	-	PFC overvoltage does not damage the bus capacitor.

Table 4-2 Output protection characteristics

Parameter	Output	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
Output overvoltage protection	MV12/SV12	13	-	15	V	Latch off
	MV6	13	-	15	V	Latch off. In MV6 mode, if the PSU loop is detected to be open, protection is triggered directly. (Reaching the MV6 overvoltage protection threshold is not required.)
Output overcurrent protection	MV12	110	-	130	%	Latch off. Response time is greater than or equal to 100 ms. (The overcurrent protection threshold can be determined according to the actual situation. The threshold is not required to be within the range from 110% to 130%, but it cannot exceed 130%.)

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Parameter	Output	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
		130	-	150	%	Latch off. Response time is greater than or equal to 4 ms.
	SV12	50	-	-	A	Protection method: The PSU enters the MV12 mode after it experienced overcurrent in SV12 mode. The indicator blinks at 1 Hz.
	MV6	11	-	15	A	Protection method: If the MV6 output overcurrent occurs, the PSU output voltage is output in MV12 mode (No system signal is required and the indicator blinks at 1 Hz); Response time is greater than or equal to 100 ms; The PSU can enter MV6 mode only after the PSON12V# signal changes from high level to low level, and then from low level back to high level.
Output short circuit protection	MV12	150	-	-	%	Latch off. Response time is less than or equal to 500 μs maximum. (Protection must be triggered within 500 μs after the short circuit protection threshold is reached.)
	SV12	50	-	-	A	Latch off
	MV6	15	-	-	A	Latch off. Response time is less than or equal to 500 μs maximum. (Protection must be triggered within 500 μs after the short circuit protection threshold is reached.)

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Parameter	Output	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
Overtemperature protection	-	55	-	-	°C	● The PSU can be protected against overtemperature and shut down only when the ambient temperature is higher than 55°C (131°F). Overtemperature protection can be automatically restored. The hysteresis for restoration must be greater than 5°C (41°F). ● The environment protection threshold of the PSU must be set to 70°C (158°F) or higher. ● If the PSU experiences overtemperature in MV12 mode, it will output based on MV6 mode (although the PSON12# signal is at low level). When the temperature of the components inside the PSU restores to normal, the PSU restores to output based on MV6 mode first (although the PSON12V# signal is at low level), and then restores to MV12 mode if the PSON12# signal is at low level. ● If the PSU experiences overtemperature in MV6 mode, it will shut down output. When the temperature of the components inside the PSU restores to normal, the PSU restores to MV6 mode first (although the PSON12V# signal is at low level), and then restores to MV12 mode if the PSON12V# signal is at low level. ● The hysteresis for restoration must be greater than 5°C (41°F).

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NOTE

- MV12/MV6/SV12 overvoltage protection unlock mode (unlocked if any of the following conditions is met):
  - AC input is recovered after it is disconnected for 15s, and the PSU can work normally. (In a parallel system, if output overcurrent occurs on one PSU, recover the power after the AC input is disconnected. The PSU can be unlocked within 2s.)
  - INSTALLED# changes from low level to high level.
  - PSON12V# changes from high level to low level.
  - IP PRESENT signal changes from low level to high level twice within 30s.
- MV12 output overcurrent/short circuit protection unlock mode (unlocked if any of the following conditions is met):
  - AC input is recovered after it is disconnected for 15s, and the PSU can work normally. (In a parallel system, if output overcurrent occurs on one PSU, recover the power after the AC input is disconnected. The PSU can be unlocked within 2s.)
  - INSTALLED# changes from low level to high level.
  - PSON12V# changes from high level to low level. After PSON12V# changes to high level for more than 500 ms and then changes to low level, the PSU can be unlocked (MV12 output overcurrent).
  - IP PRESENT signal changes from low level to high level twice within 30s.
- SV12 output overcurrent/short circuit protection unlock mode (unlocked if any of the following conditions is met):
  - AC input is recovered after it is disconnected for 15s, and the PSU can work normally.
  - INSTALLED# changes from low level to high level.
  - If SV12 triggers overcurrent protection but does not cause PSU shutdown, IP PRESENT signal changes from low level to high level once to unlock the PSU. If SV12 triggers overcurrent protection and causes PSU shutdown, IP PRESENT signal changes from low level to high level twice within 30s. After IP PRESENT signal changes to low level for more than 500 ms and then changes to high level, the PSU can be unlocked (SV12 output overcurrent).
- MV6 output overcurrent/short circuit protection unlock mode (unlocked if any of the following conditions is met):
  - AC input is recovered after it is disconnected for 15s, and the PSU can work normally.
  - INSTALLED# changes from low level to high level.
  - PSON12V# changes from high level to low level.
- After the PSU triggers fault protection, the MCU and I2C in the PSU can work properly.
- If the output exceeds the rated load, the output regulated voltage precision can be ignored.
- When the PSU output mode is switched from MV12 to MV6, all loads connected to MV12 will passively exit before the output voltage drops to 9 V. Then, the PSU only connects to MV6 loads.

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5 Parallel Operation

Current Share Design Requirements

- Supports 1+1, 2+2, or 3+3 mode. Current sharing only in MV12 mode.
- The total load at startup in parallel operation scenarios should be less than the rated load of a single PSU.
- When PSUs work in 1+1 or 2+2 mode, if one of the PSUs works in MV6 mode and other PSUs work in MV12 mode, the PSUs should work properly without affecting each other.
- In scenarios where different PSUs are not installed together, the I-MON signals of the two PSUs should not affect the normal operation of the PSUs when they are directly connected.
- Current sharing imbalance only in the normal temperature condition.

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6 Hot Swap Requirements

Hot swapping is the process of inserting the PSU to and extracting another PSU from a power system. The PSU should meet hot-swapping specifications.

The PSU can be hot swapped by the following methods:

- PSU hot swap without AC power applied (The PSU does not work before the insertion or extraction.)
  - Extraction: The AC power is disconnected from the PSU before the PSU is extracted from the system.
  - Insertion: The PSU is inserted into the system without power applied. Then, power is applied after the insertion.
- PSU hot swap through the system processor
  - Extraction: The system processor removes the PSU by turning off the PSON12V# signal.
  - Insertion: After a PSU is inserted into the system, the system processor queries the system status and turns on the PSON12V# signal to enable the PSU to work in main route operation mode.
- PSU hot swap with AC power applied (The AC/240HVDC is valid before being removed and inserted.)
  - Extraction: The PSU is removed from the system with power on.
  - Insertion: The PSU starts to work immediately after it is inserted into the system.

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7 Energy Saving Feature

7.1 Active/Standby (Hot Standby) Power Supply

7.2 Deep Sleep Function

7.3 Cold Standby Energy Saving Function

7.1 Active/Standby (Hot Standby) Power Supply

The PSUs work in parallel and enter the active/standby mode.

- The system sets the output voltage of one PSU to the voltage (12.05 V) of the standby PSU through the **0x21 VOUT_COMMAND** command.
- The **0xCE PS_CONTROL** command bit 7 is set to **1**, and the PSU with an output setting voltage of 12.3 V is the active PSU (only MV12 supports hot standby).

Exit the standby PSU mode:

- The **0xCE PS_CONTROL** command bit 7 is set to 0.
- The output voltage of the standby PSU is set to setting voltage through the **0x21 VOUT_COMMAND** command.

Table 7-1 Active/Standby power supply characteristics

Parameter	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
12 V output voltage of the standby PSU	11.7	-	-	V	T_A = 25°C (77°F)
No-load power consumption of the standby	-	-	8	W	• 1+1 power supply scenario: For the active/standby power supply with rated input of 115 V AC/230 V AC/240

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Parameter	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
PSU (test when the standby PSU has no load)	-	-	8	W	V DC (AC+AC, AC+HVDC, HVDC +HVDC), the input power consumption of each standby PSU is lower less than 8 W (when the standby PSU has no load). ● 2+2 power supply scenario: For the active/standby power supply with AC +AC, AC+HVDC, and HVDC+HVDC, the input power consumption of each standby PSU is lower less than 8 W (when the standby PSU has no load).
Busbar voltage in fault mode	11.4	-	-	V	In 1+1 active/standby power supply scenarios (AC+AC, AC+HVDC, or HVDC +HVDC), if one of the PSUs powers off and experiences a fault (such as input power failure, hot swap, input undervoltage, input overvoltage, fan fault, overtemperature, or output overvoltage), the output voltage is more than 11.4 V, and the time when the voltage is lower than 11.4 V but not lower than 11 V cannot exceed 100 µs. **NOTE** ● Exclude the scenario in which a short circuit occurs before ORing-FET. ● Exclude the scenario in which a fault occurs when the PSU is extract with power applied. ● In a parallel scenario, the output power is less than 1000 W, and the 12 V output is connected with a 540 µF capacitor. ● In a parallel scenario, the output power is more than 1000 W, and the 12 V output is connected with a 1000 µF capacitor.

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Parameter	Minimum Value	Typical Value	Maximum Value	Unit	Remarks
	11.4	-	-	V	2+2 active/standby power supply scenario (AC+AC, AC+HVDC, or HVDC +HVDC): The output voltage is more than 11.4 V, and the time when the voltage is lower than 11.4 V but not lower than 11 V cannot exceed 100 μs. ● One power input fails (2+2 backup, one power input for two PSUs). ● One of the PSUs experienced a fault (such as input undervoltage, input overvoltage, fan fault, overtemperature, output overvoltage, or hot swap). **NOTE** ● Exclude the scenario in which a short circuit occurs before ORing-FET. ● Exclude the scenario in which a fault occurs when the PSU is extract with power applied. ● In a parallel scenario, the 12 V output is connected with a 1500 μF capacitor.

NOTE

- In the non-N+R active/standby power supply scenario, the total load should be less than 70% of the rated power.
- After the PSU enters the standby mode, the system set the bit7 of the **CEh (PS_CONTROL)** command to 0. The single-chip microcomputer of the PSU powers off, restarts, and the PSU exits the standby mode in power failure scenario. PSU does not exit the standby mode in other scenarios. When the output load exceeds the failover capability of the standby PSU when a fault occurs, the PSU is allowed to exit the standby mode.

7.2 Deep Sleep Function

The PSU supports the deep sleep function. The PSU enters deep sleep mode if bit B of **PS_CONTROL (CEh)** is set to 1.

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NOTE

When PSUs are connected in parallel, if one PSU enters deep sleep mode and the system communicates with it, the PSU input information including input voltage, current, and power is reported as 0, and the output information is reported as 0 (the output voltage and current are reported as 0).

- The deep sleep function is available in both MV12 and MV6 modes.
- In deep sleep mode, the PSU input power is less than 1 W.
- The main power is shut down and MV12/SV12 has no output.
- The PFC is shut down.
- The fan obtains power from the 12 V bus.
- The power control function can be obtained through the 12 V bus.
- The output of all converters can be controlled online and restored to normal.

In a parallel system, the PSU is not allowed to enter deep sleep mode unless all of the following occur:

- The system sets the deep sleep control register to 1.
- EFUSEV is less than or equal to 0.8 V.

The PSU must be able to restore output of converters when any of the following occurs:

- The bus voltage is less than 5.7 V.
- The deep sleep control register is set to 0.
- EFUSEV is greater than 1.0 V.

7.3 Cold Standby Energy Saving Function

In a 1+1 redundancy system, the cold standby mode is supported when the output mode is MV12.

The PSU can enter cold standby mode if all of the following conditions are met:

- The load is less than 70% rated load of a single PSU.
- The system sends a D0h 0x01 command to set one of the PSUs as the active PSU (SMART_ON changes to high level).
- The system sends D0h 0x02/0x03/0x04 to the other PSU and detects that SMART_ON is high.

The cold standby PSU exits cold standby mode if any of the following conditions is met:

- The system sends the D0h 0x00 command.
- The single-chip microcomputer of the PSU restarts.
- The I2C communication of the PSU is abnormal.
- The MV12/SV12 output of the active PSU encounters a fault, such as input power failure, input overvoltage/ undervoltage, output overvoltage/overcurrent/ undervoltage (< 11.8 V), overtemperature, and fan fault, which causes SMART_ON to change to low level.
- The PSON12V# signal changes from low level to high level, and the INSTALLED# signal changes from low level to high level.

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- The load power is greater than the maximum power supported by cold standby.

The active PSU exits cold standby mode if any of the following conditions is met:

- The system sends the D0h 0x00 command.
- The single-chip microcomputer of the PSU restarts.
- The I2C communication of the PSU is abnormal.
- The 12 V output of the active PSU encounters a fault, such as input power failure, input overvoltage/ undervoltage, output overvoltage/overcurrent/ undervoltage (< 11.8 V), overtemperature, and fan fault.
- The PSON12V# signal changes from low level to high level, and the INSTALLED# signal changes from low level to high level.

Table 7-2 Power consumption of standby PSU in cold standby mode

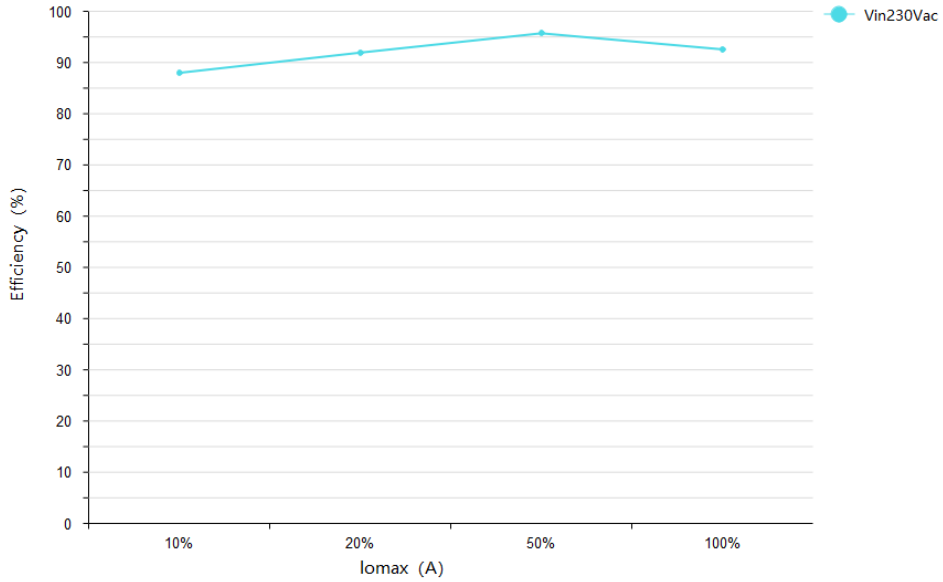
Parameter	Min.	Typ.	Max.	Unit	Notes & Conditions
Power consumption of standby PSU	-	-	8	W	In 1+1 active/standby power supply scenarios (AC+AC, AC+HVDC, HVDC +HVDC), the input power consumption of each standby PSU must be tested without any load.

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8 Feature Curves

Working condition: T_A = 25°C (77°F) unless otherwise specified

Figure 8-1 Efficiency curve (V_{in} = 230 V AC)



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Figure 8-2 Efficiency curve (V_{in} = 115 V AC)

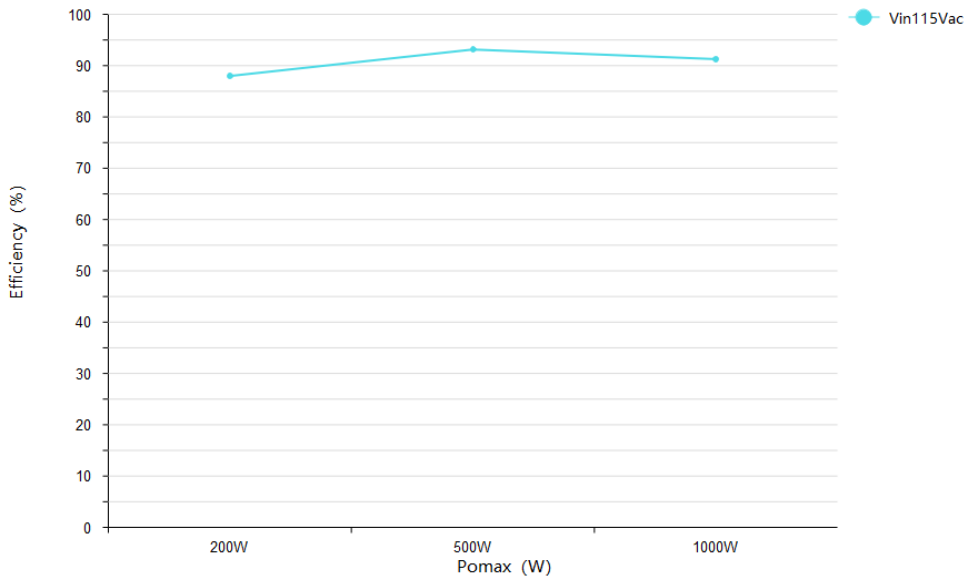
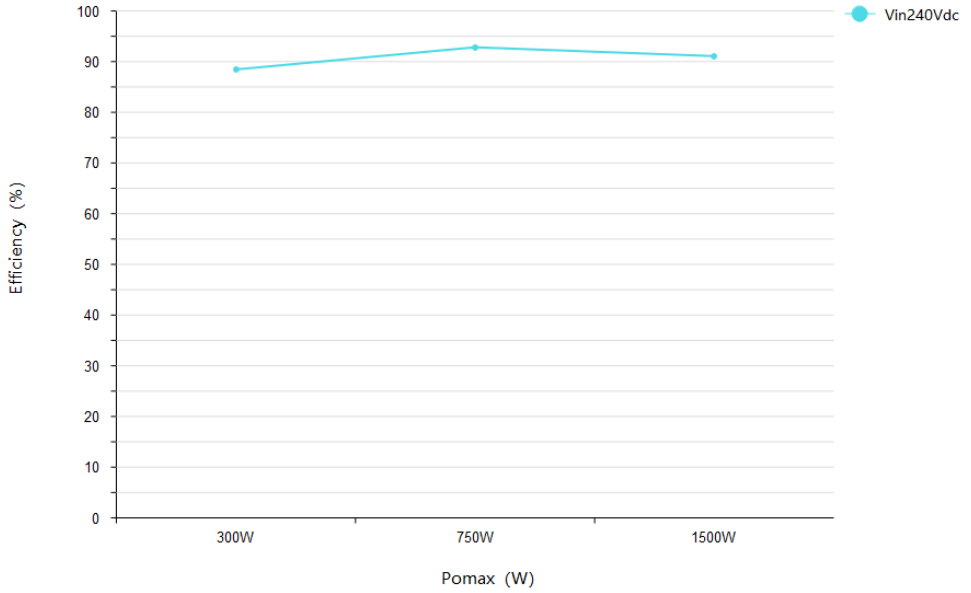


Figure 8-3 Efficiency curve (V_{in} = 240 V DC)



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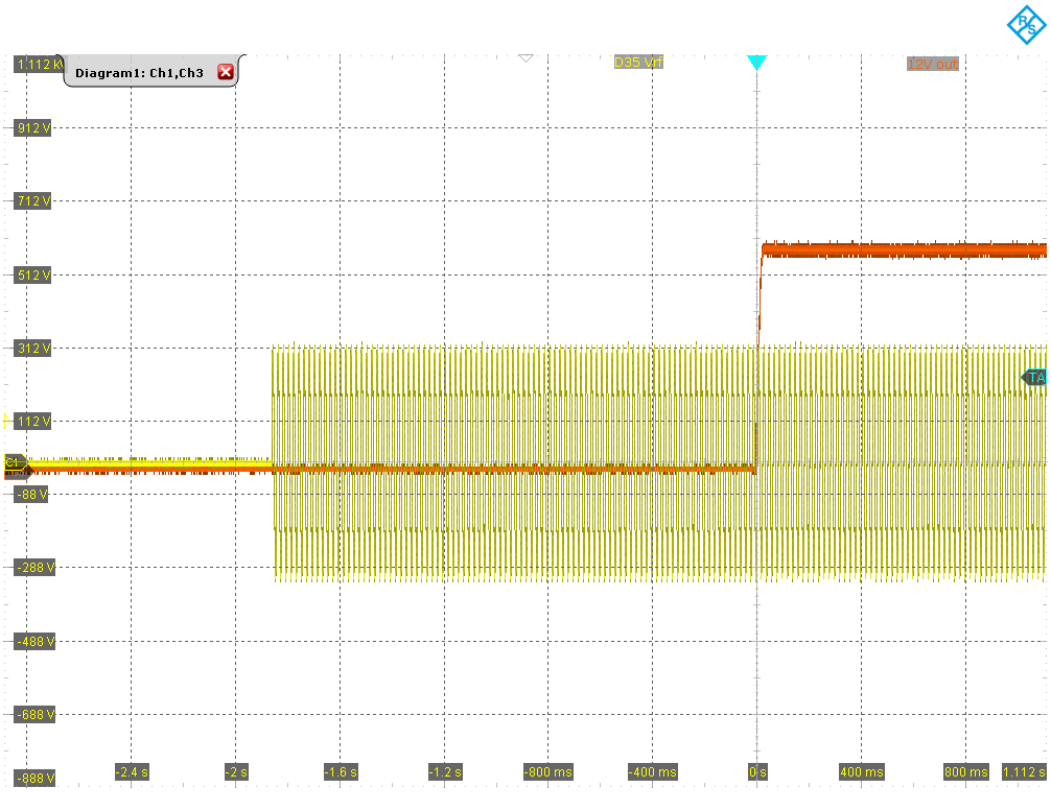
9 Typical Waveforms

9.1 Turn-on/Turn-off
9.2 Output Voltage Dynamic Response
9.3 Output Voltage Ripple and Noise

9.1 Turn-on/Turn-off

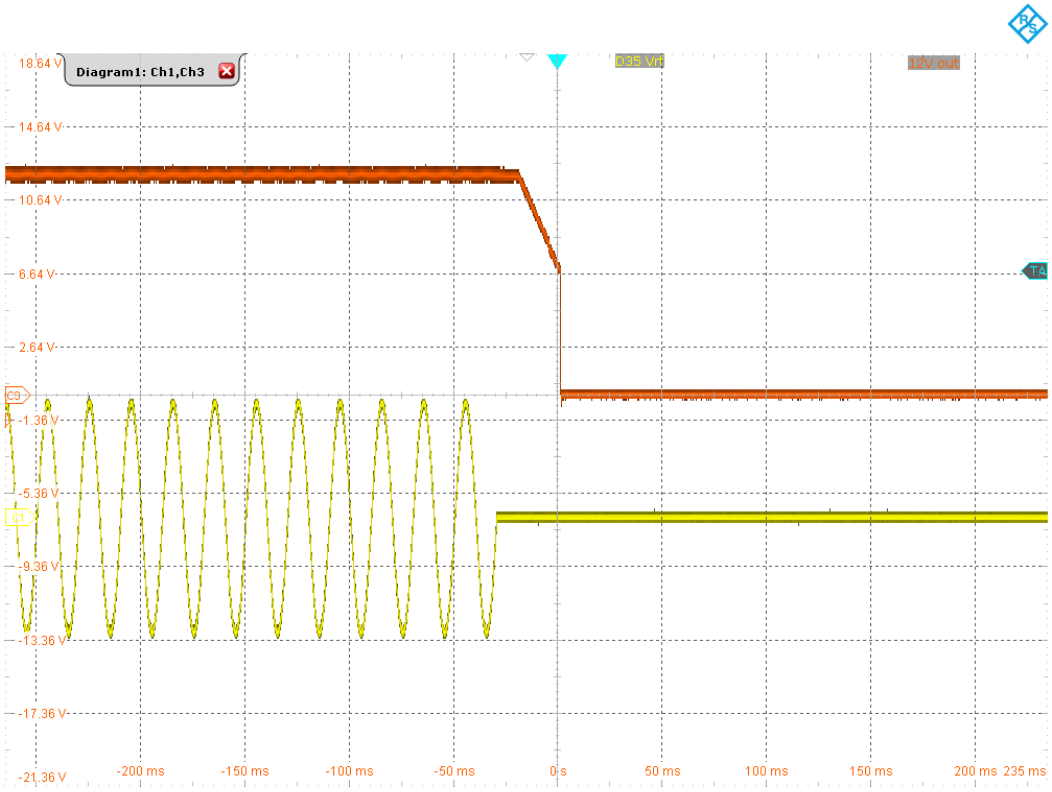
Working condition: T_A = 25°C (77°F) unless otherwise specified.

Figure 9-1 Turn-on waveform (V_{in} = 220 V AC)



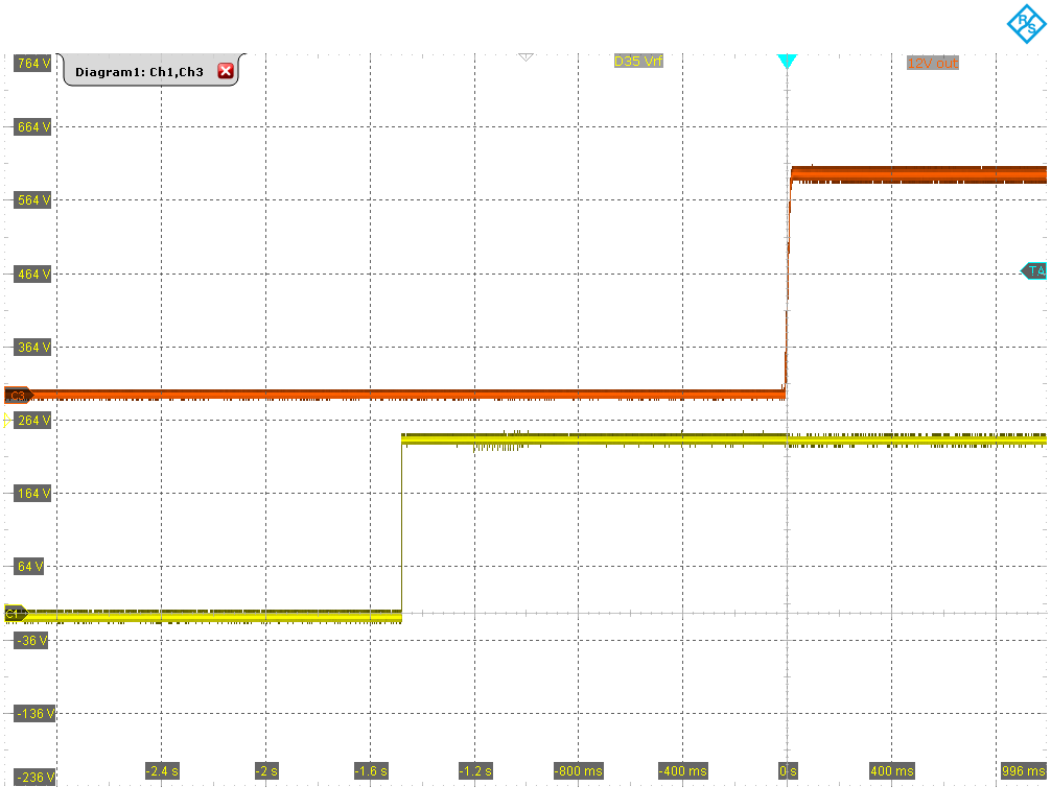
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Figure 9-2 Turn-off waveform (V_{in} = 220 V AC)



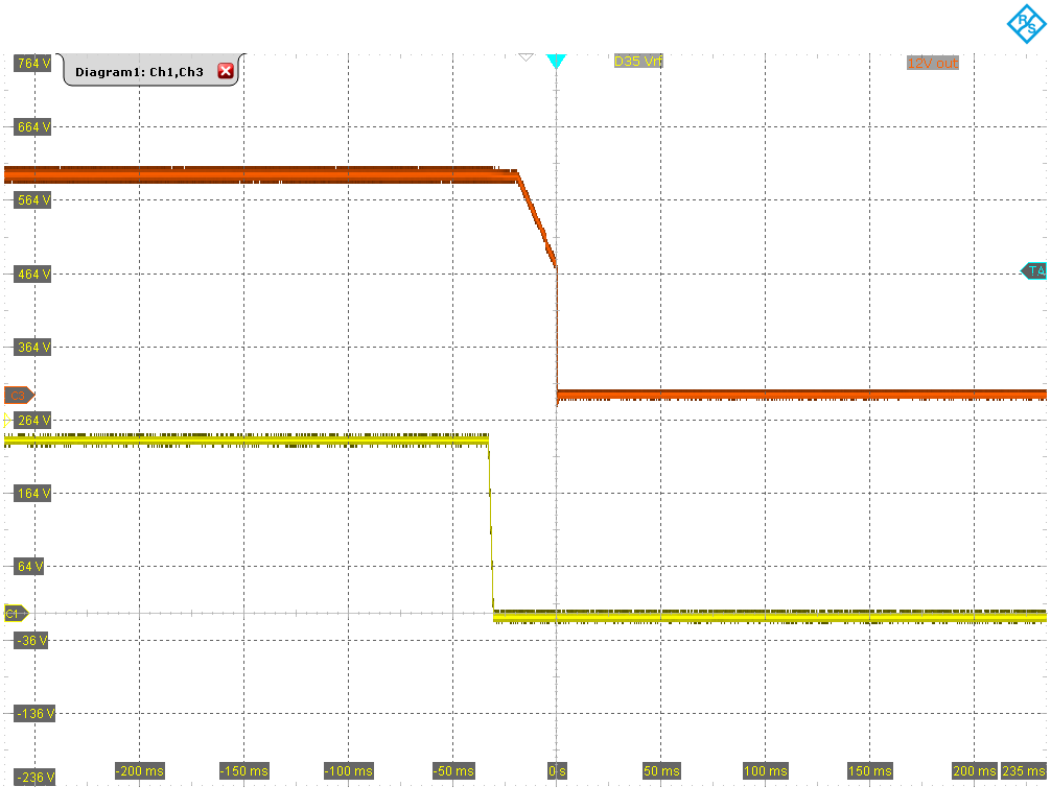
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Figure 9-3 Turn-on waveform (V_{in} = 240 V DC)



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Figure 9-4 Turn-off waveform (V_{in} = 240 V DC)

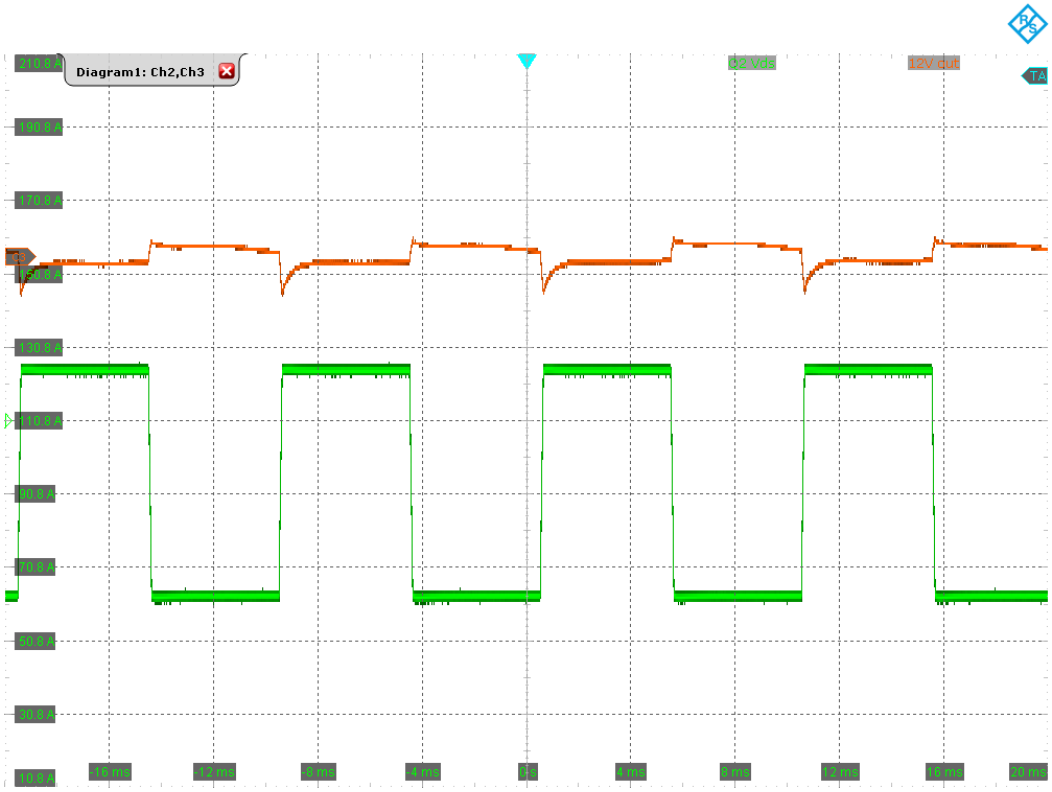


9.2 Output Voltage Dynamic Response

Working condition: T_A = 25°C (77°F) unless otherwise specified.

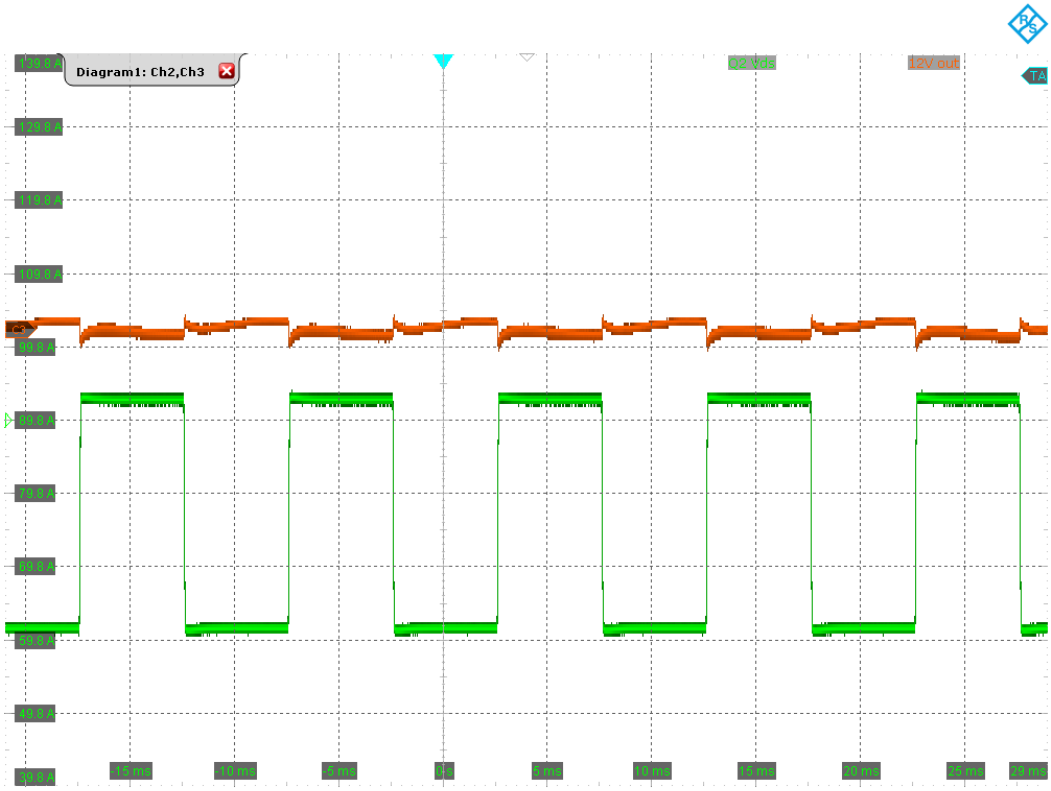
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Figure 9-5 Output voltage dynamic response (V_{in} = 220 V AC; load: 50%–100%–50%)



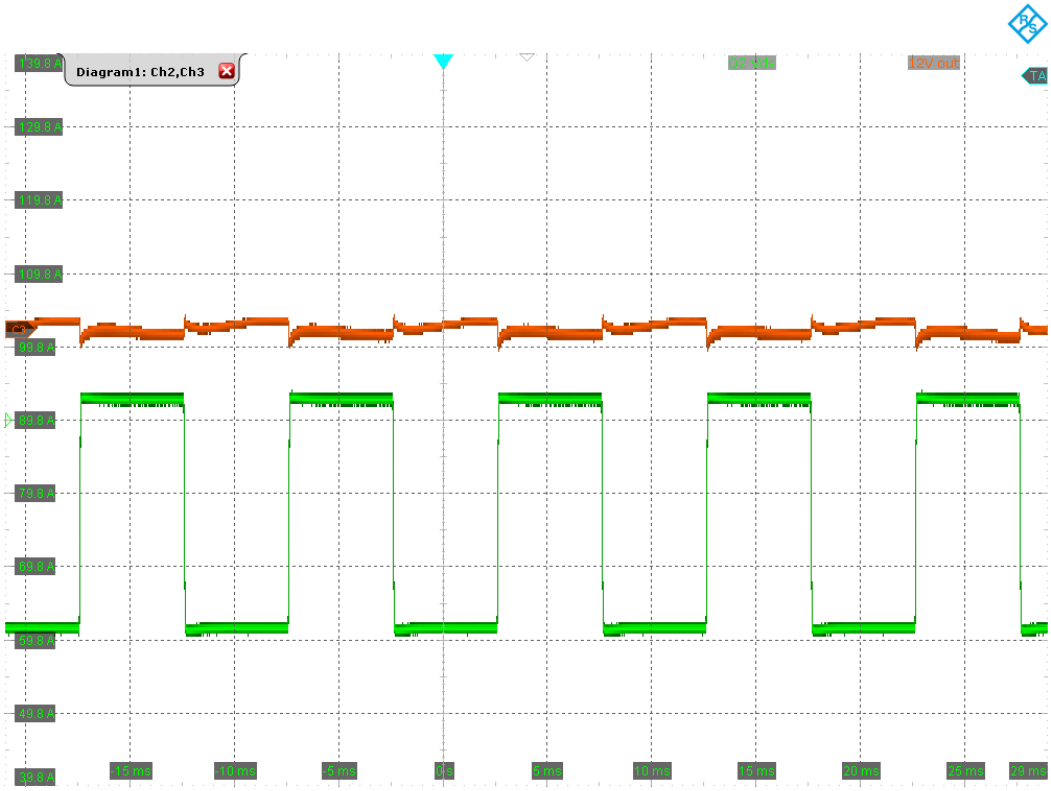
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Figure 9-6 Output voltage dynamic response (V_{in} = 220 V AC; load: 25%–50%–25%)



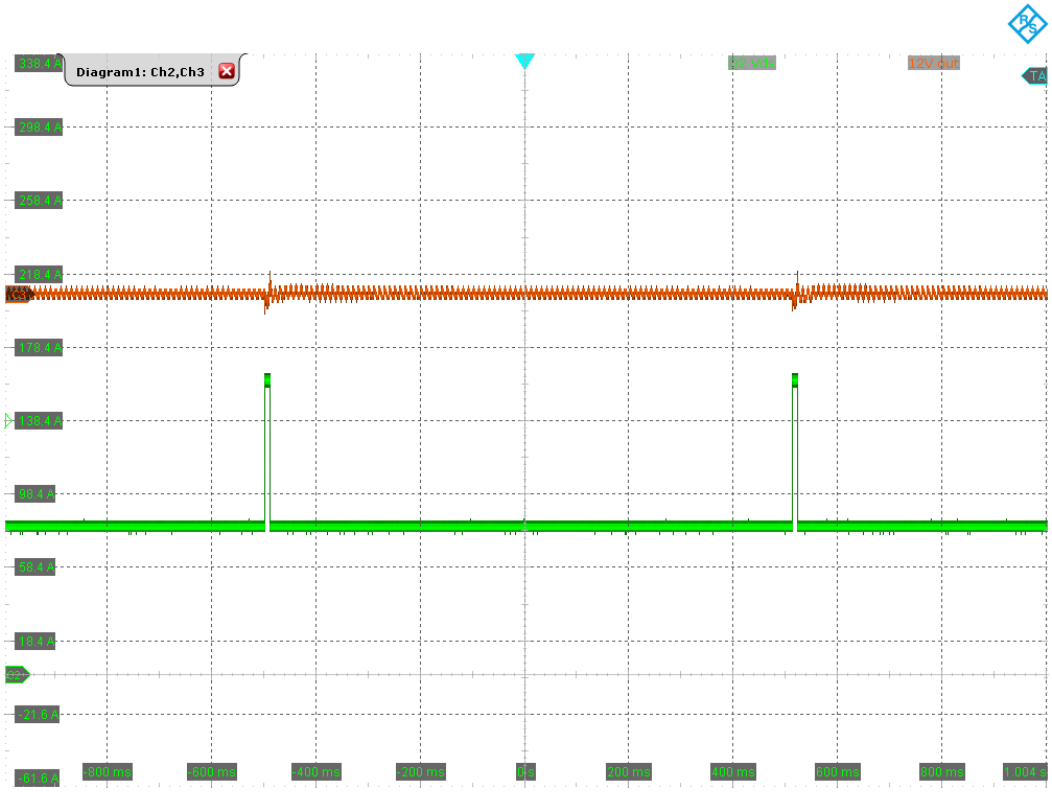
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Figure 9-7 Output voltage dynamic response (V_{in} = 220 V AC; load: 50%–75%–50%)



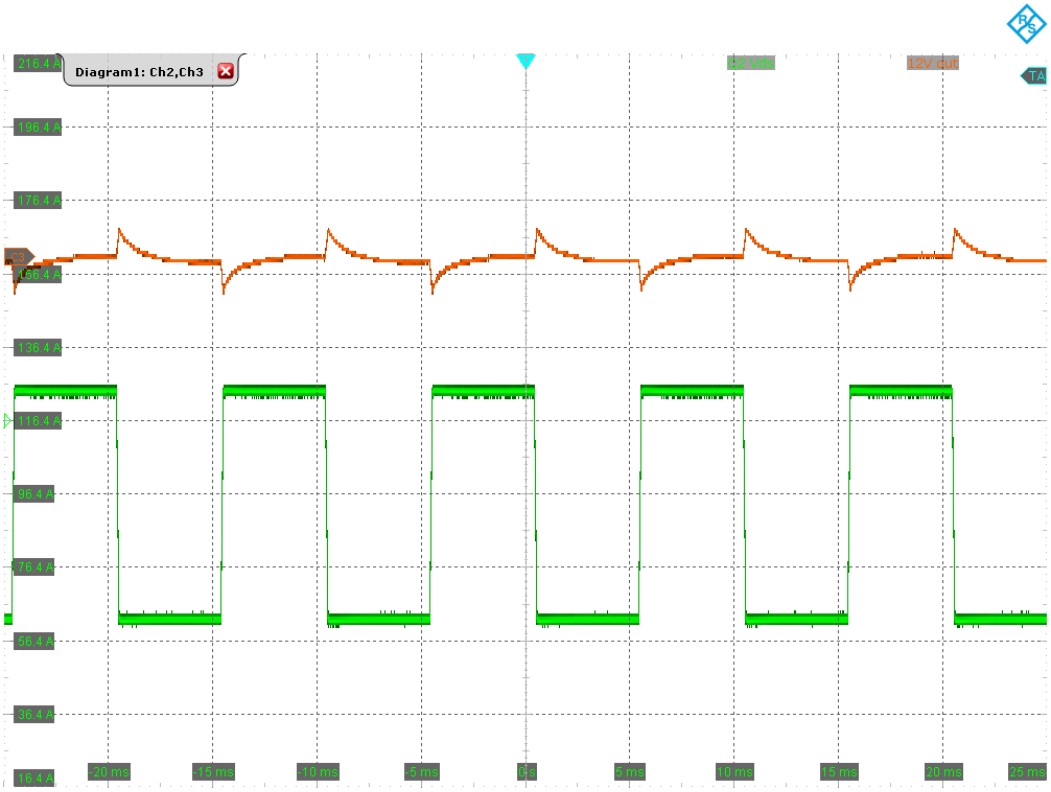
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Figure 9-8 Output voltage dynamic response (V_{in} = 220 V AC; load: 65%–130%–65%)



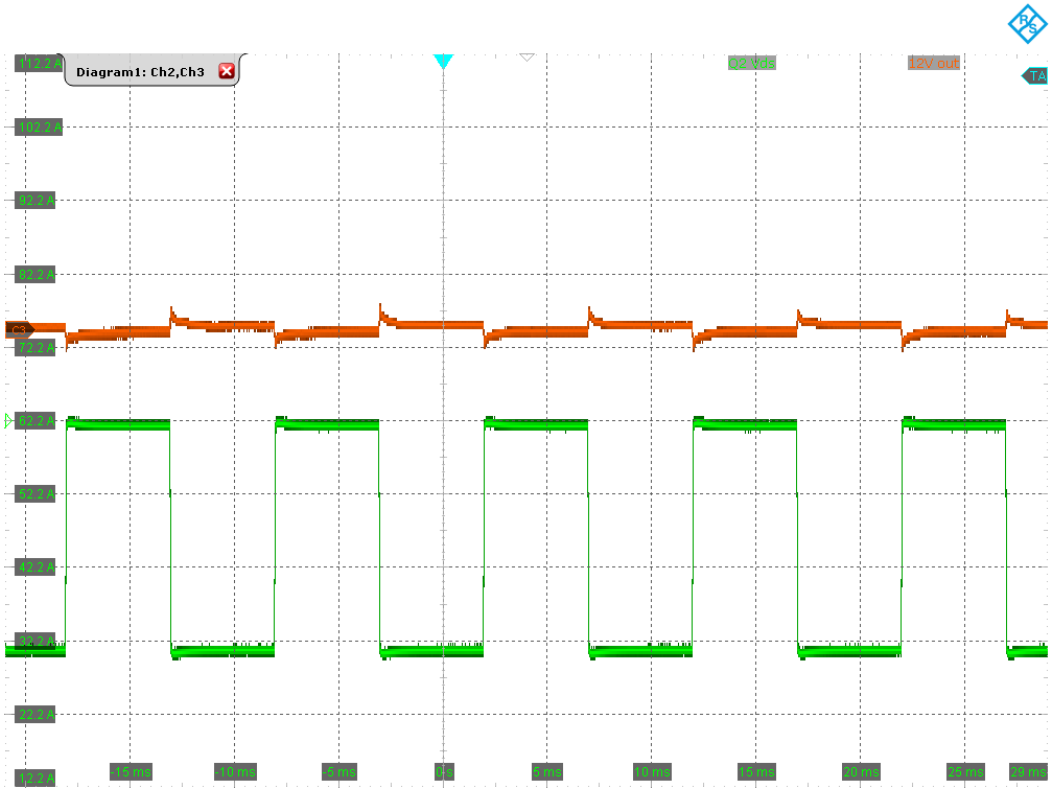
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Figure 9-9 Output voltage dynamic response (V_{in} = 240 V DC; load: 50%–100%–50%)



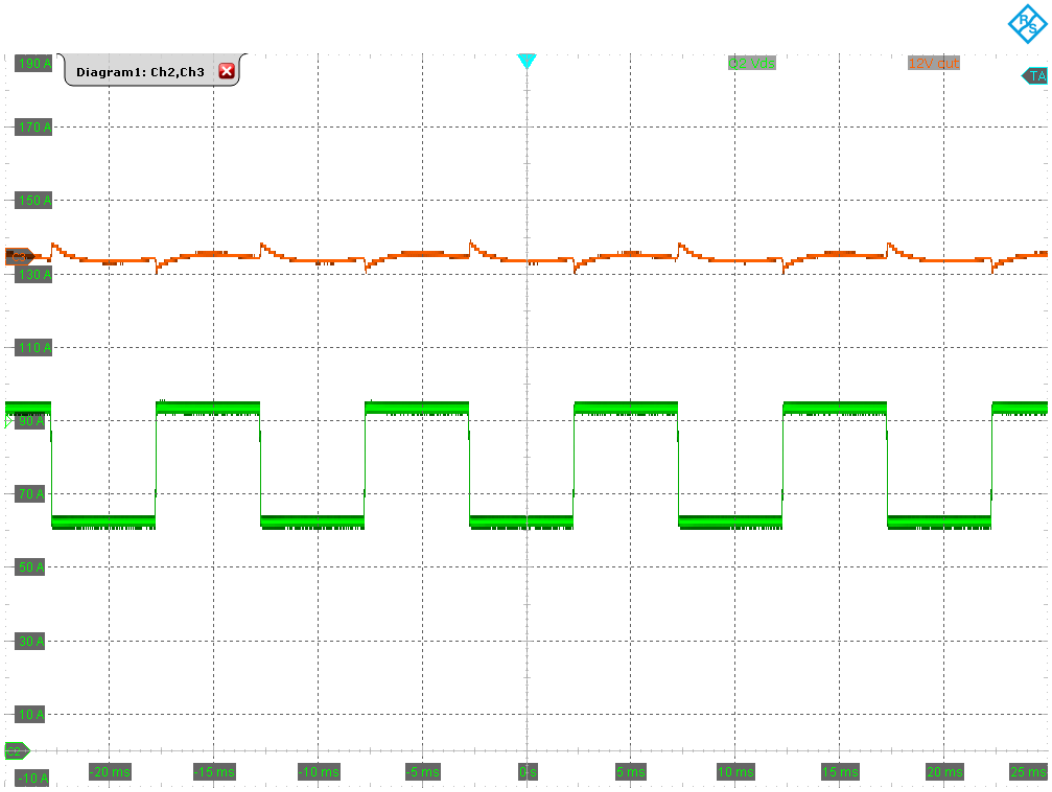
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Figure 9-10 Output voltage dynamic response (V_{in} = 240 V DC; load: 25%–50%–25%)



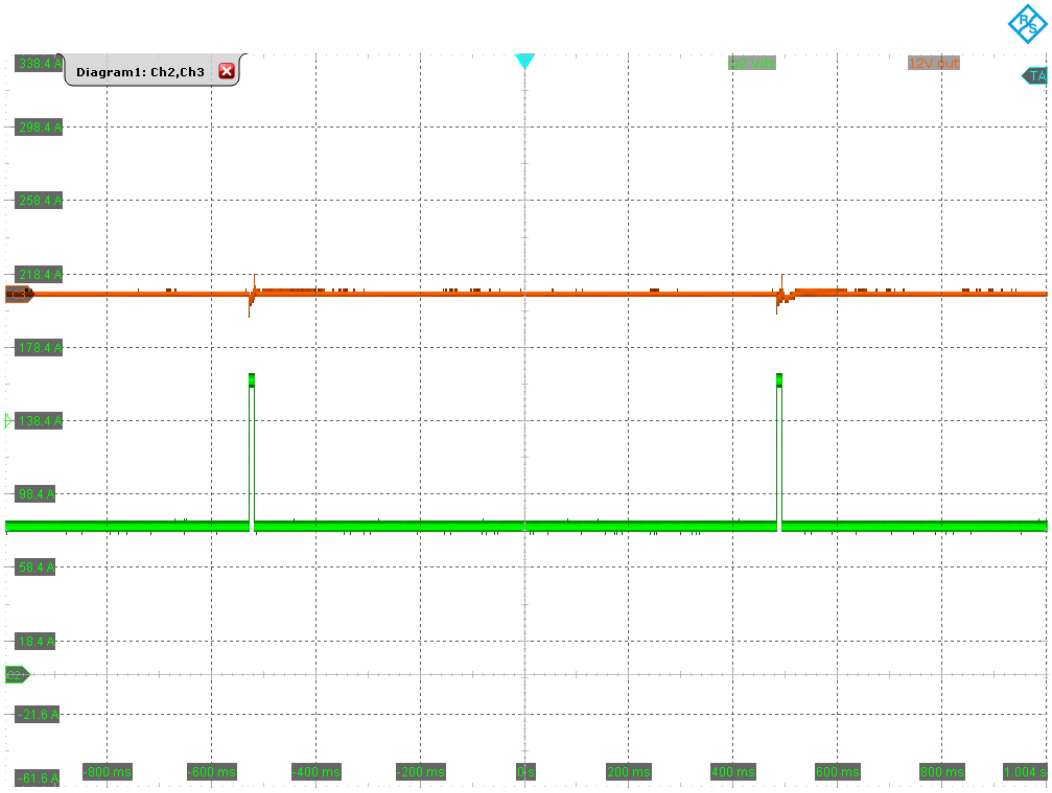
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Figure 9-11 Output voltage dynamic response (V_{in} = 240 V DC; load: 50%–75%–50%)



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Figure 9-12 Output voltage dynamic response (V_{in} = 240 V DC; load: 65%–130%–65%)

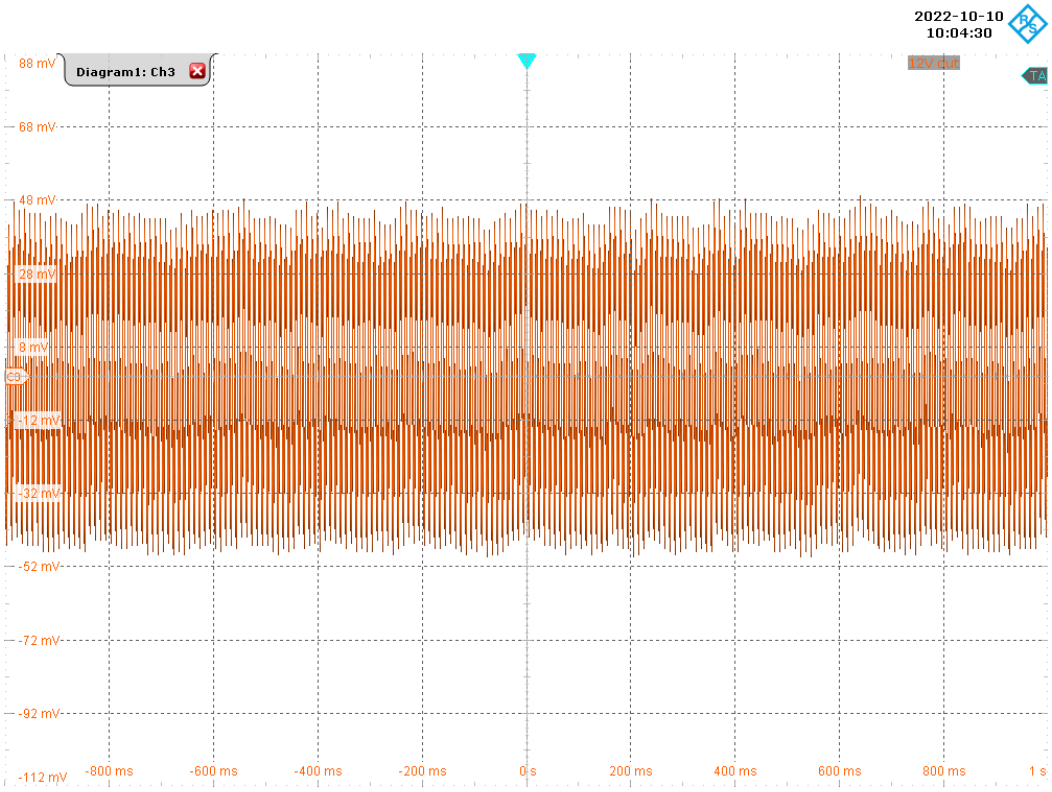


9.3 Output Voltage Ripple and Noise

Working condition: T_A = 25°C (77°F) unless otherwise specified.

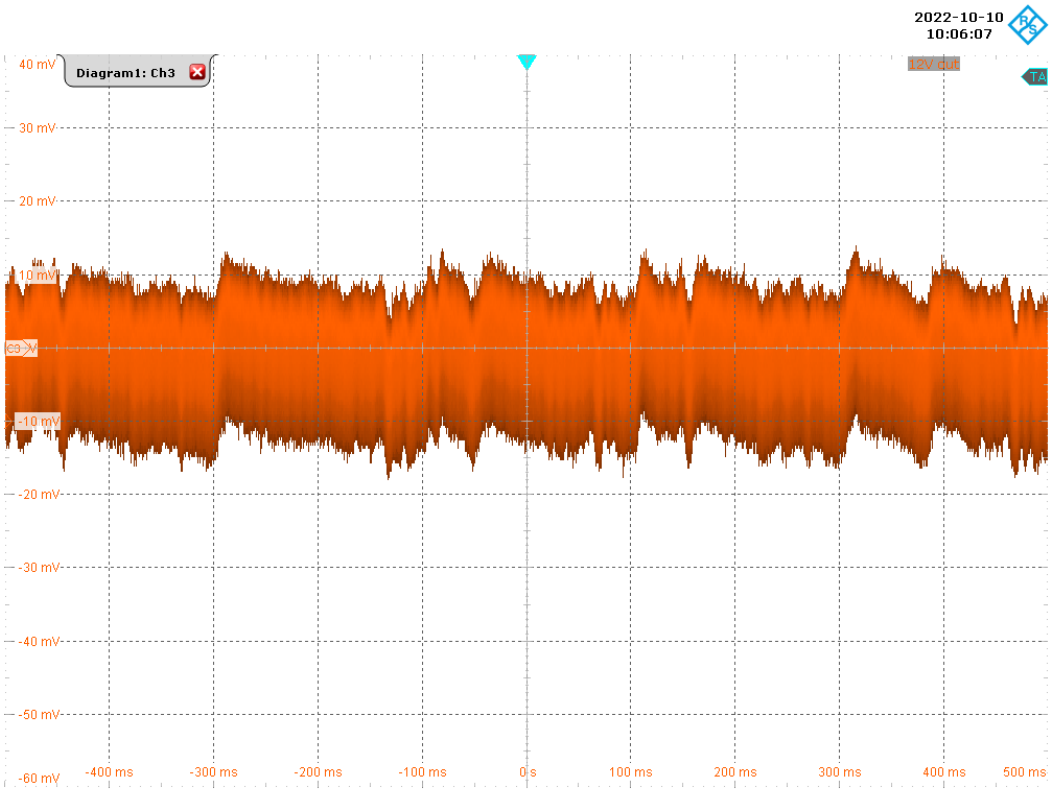
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Figure 9-13 Output voltage ripple (V_{in} = 220 V AC)



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Figure 9-14 Output voltage ripple (V_{in} = 240 V DC)



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10 Control Signals

Signal noise (golden finger output connector): The peak voltage is less than 250 mV (20 MHz bandwidth).

10.1 PSU Timing Diagram

10.2 ORing-FET

10.3 PSON12V#

10.4 PRESENT#

10.5 INSTALLED#

10.6 OPOK

10.7 IPOK

10.8 IPOK LINK

10.9 IP PRESENT

10.10 CYC_PWR#

10.11 I-MON

10.12 PS_INTERRUPT

10.13 EFUSEV

10.14 SMART_ON

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10.1 PSU Timing Diagram

Figure 10-1 PSU timing diagram

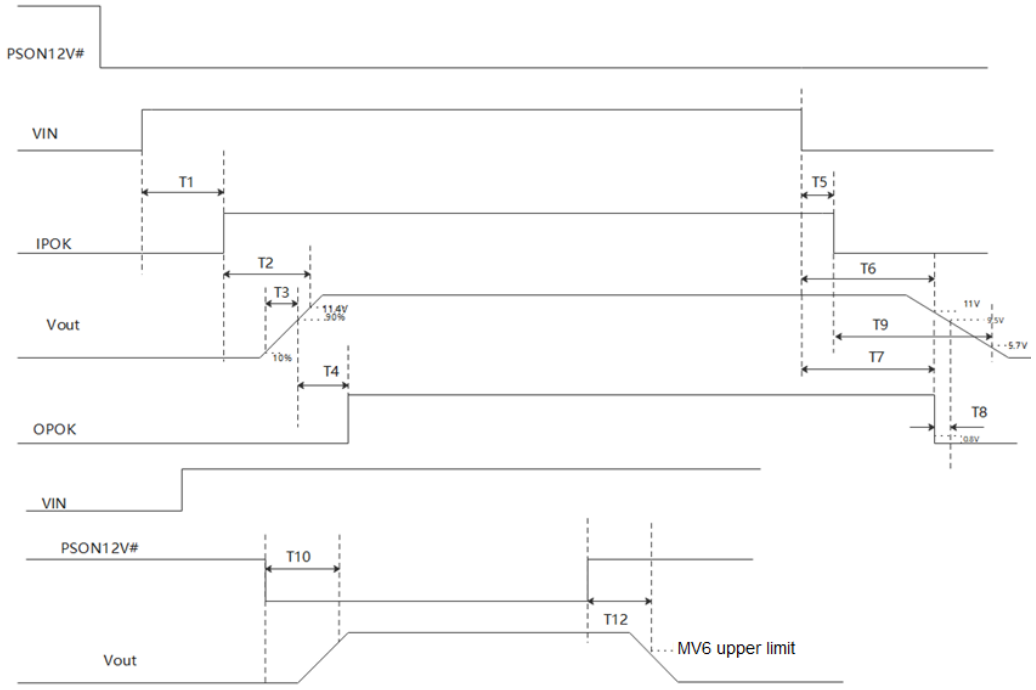


Table 10-1 Output PSU timing requirements

Mark	Description	Min.	Max.	Unit
T1	Time from AC/DC input to when IPOK is at high level	-	2000	ms
T2	Time from when IPOK is at high level to when MV12 output to 11.4 V (PSON12V# is at low level)	-	500	ms
T3	Time for MV6 output to increase from 10% to 90%	5	25	ms
	Time for MV12 output to increase from 10% to 90%	5	25	ms
T4	Delay time for the OPOK to take effect (when MV12/MV6 increases to 90% to when OPOK is at high level)	50	100	ms
T5	IPOK failure delay time (from input outage to low IPOK signal, circuit breaker disconnection not considered, full load test conditions)	-	4	ms
T6	Output hold-up time 1 (from input power failure during normal running to 11 V output, 80% load, DIP performance)	10	-	ms

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Mark	Description	Min.	Max.	Unit
T7	OPOK hold-up time (from AC power failure to low OPOK signal, 80% load)	10	-	ms
	Time from AC power failure to low OPOK signal, 40% load	20	-	ms
	Time from AC power failure to low OPOK signal, 25% load	30	-	ms
	Time from AC power failure to low OPOK signal, 12.5% load	40	-	ms
T8	Duration from the time when the OPOK signal falls to 1.43 V to the time when the 12 V bus voltage drops to 9.5 V	200	-	μs
T9	Output hold-up time 2 (the IPOK output power failure runs to 5.7 V that is less than 9.2 V after normal input power failure, and the power is 15 W with the power failure logs of T6.)	300	-	ms
T10	Delay time for the PSON12V# to change MV6 to MV12 mode (PSON12V# signal runs to 11.4 V.)	80	200	ms
	Time for MV6 OCP or MV12 OTP recovery (MV6 rises to 11 V.)	2.1	3.3	s
T12	Time for MV12 to drop to MV6 mode (with 24 W load)	80	200	ms

NOTE

- In case of input power failure, the PSU needs 10 ms to 120 ms to set IIC alarm position to 1 at more than half load, and the system needs 20 ms to 120 ms to set IIC alarm position to 1 at below half load (including half load).
- The storage NVDIMM power backup requirement is supported.
  - T8: This is required in the case of input power failure, input overvoltage/undervoltage, fan fault, overtemperature, and output undervoltage, and is not required in the case of OPOK alarms caused by output overcurrent/short-circuit and output overvoltage; if input power failure, undervoltage, or overvoltage occurs, the OPOK voltage should not rebound after dropping.
  - The PSU can report the OPOK fault in advance if the fault can be predicted by the PSU (such as input power failure, input overvoltage/undervoltage, fan fault, overtemperature, or output undervoltage). In this case, the requirement that the output voltage must be less than 11.4 V does not have to be met.
- The test of T1 and T2 requires that the auxiliary power supply be completely powered off.
- Test T3 when the load is higher than the minimum value.
- PSON12V# reliability: If the PSON12V# signal at high level is tested greater than or equal to 25 ms, this signal is valid.

10.2 ORing-FET

PSU side:

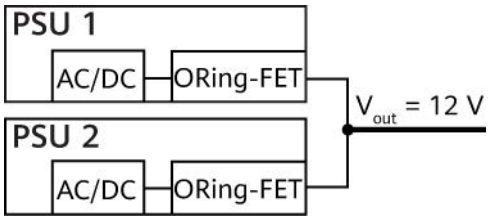
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An ORing-FET failure isolation circuit needs to be added inside the PSU 12 V output. In the practical application scenario (1+1 and 2+2 redundancy), if one PSU experiences an internal fault, it should exit automatically to prevent the bus voltage output from being abnormal.

System side:

- Capacitance at the MV12 input side: The equivalent capacitance should be greater than the PSU's minimum capacitive load 540 μF multiplied by N (N is the number of PSUs configured in the system) and less than the PSU's maximum capacitive load 8000 μF.
- The 12 V load should be greater than the minimum PSU load 3 A and less than the rated load of the PSU.

Figure 10-2 ORing-FET circuit



10.3 PSON12V#

The PSON12V# signal is an on/off signal that controls the PSU 12 V output remotely. If the PSON12V# signal is at low level and takes effect, the PSU 12 V enters MV12 output mode. If the PSON12V# is at high level or left open, the PSU enters MV6 output mode. If PSON12V# is at high level for 25 ms or longer, PSON12V# is valid.

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Figure 10-3 Interconnect diagram of the PSON12V# signal

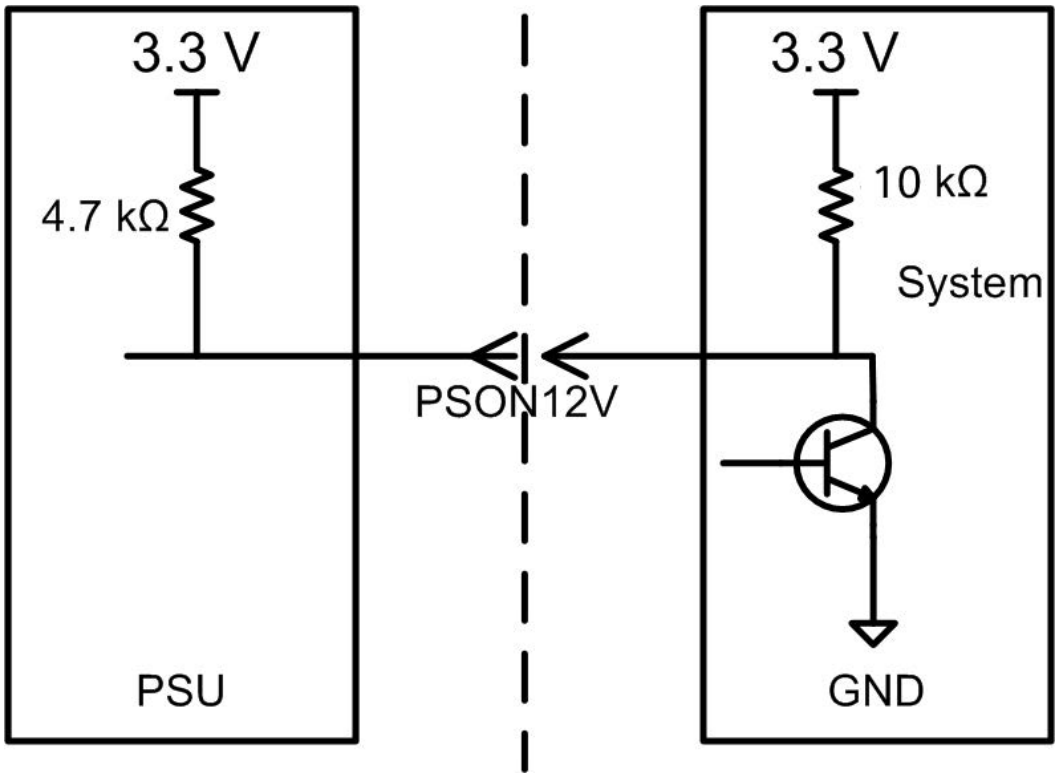


Table 10-2 PSON12V# signal characteristics

Signal Characteristics	
Signal type	PSU side: This signal is pulled up to the 3.3 V auxiliary power source inside the PSU by a 4.7-kilohm resistor. System side: This signal is output by CPLD over the drive circuit and pulled up to 3.3 V by a 4.7-kilohm resistor.
PSON12V# = Low	MV12 output mode
PSON12V# = High/Open	MV6 output mode

Table 10-3 PSON12V# output

PSON12V# Output	Minimum Value	Maximum Value
Low level voltage (main output enabled)	0 V	0.8 V
High level voltage (main output disabled)	2.0 V	3.465 V
Source current, and PSON12V# = Low	-	4 mA

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PSON12V# Output	Minimum Value	Maximum Value
Signal rise and fall time	-	200 μs

10.4 PRESENT#

The PRESENT# signal is used by the system to detect whether the PSU is present. The signal pin is required to be shorter than other pins on the PSU. The pin is grounded inside the PSU.

Table 10-4 PRESENT# signal characteristics

Signal Characteristics	
Signal type	PSU side: The pin is directly grounded inside the PSU. System side: This signal is pulled up to 3.3 V by a 4.7-kilohm resistor and output to the system CPLD over a series resistor.
PRESENT# = Low	PSU is present.
PRESENT# = High	PSU is removed from the system.

10.5 INSTALLED#

The INSTALLED# signal is used by the PSU to determine whether it has been inserted into the system. The signal pin is required to be shorter than other pins on the PSU.

Table 10-5 INSTALLED# signal characteristics

Signal Characteristics	
Signal type	PSU side: This signal is pulled up to the 3.3 V auxiliary power source inside the PSU by a 4.7-kilohm resistor. System side: This pin is grounded directly.
INSTALLED# = Low	PSU is inserted into the system.
INSTALLED# = High	PSU is not inserted into the system.

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Table 10-6 INSATLLED# output

INSATLLED# Output	Minimum Value	Maximum Value
Low level voltage	0 V	0.8 V
High level voltage	2.0 V	3.465 V
Sink current, and INSATLLED# = Low	-	4 mA
Signal rise and fall time	-	200 µs

10.6 OPOK

The OPOK signal indicates whether the main output is normal.

Figure 10-4 Interconnect diagram of the OPOK signal

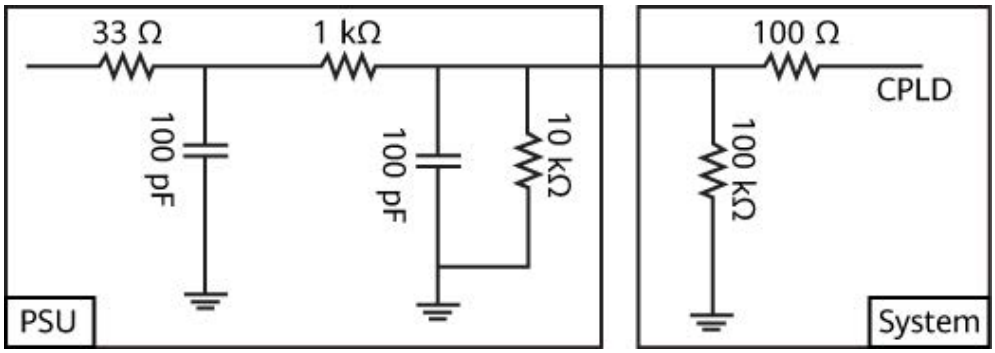


Table 10-7 OPOK signal characteristics

Signal Characteristics	
Signal type	PSU side: This signal is output over the drive circuit and pulled up to 3.3 V by a 1-kilohm resistor, or directly output by the DSP over a series resistor. System side: Pulled down to GND by a 100-kilohm resistor and connected to the system CPLD over a 100-ohm series resistor.
OPOK = High	The main output is normal.
OPOK = Low	The main output is abnormal.

Table 10-8 OPOK output

OPOK Output	Min.	Max.
Low level voltage	0 V	0.6 V

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OPOK Output	Min.	Max.
High level voltage	2.2 V	3.465 V
Sink current, OPOK = Low	-	4 mA
Source current, OPOK = High	-	4 mA
Signal rise and fall time	-	200 μs

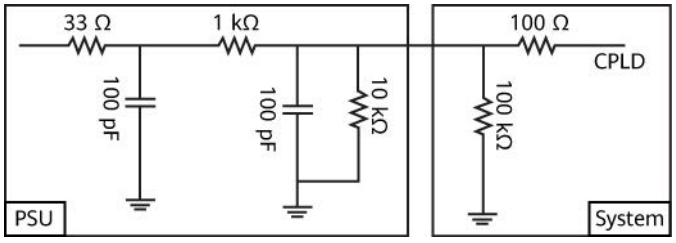
NOTE

- If the INSTALLED# signal is at high level, then the OPOK signal is at low level.
- In MV12 mode:
  - If the output voltage is higher than 11.5 V, OPOK is at high level;
  - If the output voltage is lower than 11.4 V, OPOK is at low level.
  - The time from when OPOK drops to 1.43 V to when the 12 V bus voltage drops to 9.5 V is greater than 100 μs (this is required in the case of input power failure, input overvoltage/undervoltage, fan fault, overtemperature, and output undervoltage, and is not required in scenarios such as output overcurrent or short circuit and output overvoltage).
- In MV6 mode:
  - If the output voltage is higher than 5.9 V, OPOK is at high level;
  - If the output voltage is lower than 5.8 V, OPOK is at low level.
- In the case of input power failure, input overvoltage, input undervoltage, fan fault, overtemperature, and output undervoltage, the PSU can report the OPOK fault in advance.
- After the input power fails, the OPOK signal changes to low level. The OPOK signal should not changes to high level until the input power recovers.

10.7 IPOK

The IPOK signal is used to detect whether the PSU input is normal.

Figure 10-5 Interconnect diagram of the IPOK signal



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Table 10-9 IPOK signal characteristics

Signal Characteristics	
Signal type	PSU side: The power is output after RC filtering. System side: Pulled down to GND by a 100-kilohm resistor and connected to the system CPLD over a 100-ohm series resistor.
IPOK = High	The input is normal. (90 V AC < V_{in} < 264 V AC; 180 V DC < V_{in} < 300 V DC).
IPOK = Low	The input is abnormal (after AC or DC input undervoltage/overvoltage protection).

Table 10-10 IPOK output

IPOK Output	Min.	Max.
Low level voltage	0 V	0.6 V
High level voltage	2.2 V	3.465 V
Sink current, IPOK = Low	-	4 mA
Source current, IPOK = High	-	4 mA
Signal rise and fall time	-	200 μs

10.8 IPOK LINK

The IPOK LINK signal is sent by the system to the PSU. It indicates that at least one PSU is present in the system and its input is normal.

Table 10-11 IPOK LINK signal characteristics

Signal Characteristics	
Signal type (PSU output signal)	PSU side: This signal is pulled up to the 3.3 V auxiliary power source inside the PSU by a 4.75-kilohm resistor. System side: This signal is output by CPLD over the drive circuit and pulled up to 3.3 V by a 4.7-kilohm resistor.
IPOK LINK = High	The standby power supply input is normal.
IPOK LINK = Low	The standby power supply input is abnormal.

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Table 10-12 IPOK LINK output

IPOK LINK Output	Min.	Max.
Low level voltage	0 V	0.6 V
High level voltage	2.2 V	3.465 V
Sink current, IPOK LINK = Low	-	4 mA
Source current, IPOK LINK = High	-	4 mA
Signal rise and fall time	-	200 µs

10.9 IP PRESENT

IP PRESENT is a signal that controls the SV12 switch of the PSU. After IP PRESENT is at low level (valid), the PSU enters the SV12 output state. The designed overcurrent protection threshold is greater than or equal to 50 A in this case. When the IP PRESENT signal is at high level, the PSU maintains the MV12 output mode.

Table 10-13 IP PRESENT# signal characteristics

Signal Characteristics	
Signal type	PSU side: This signal is pulled up to the 3.3 V auxiliary power source inside the PSU by a 4.75-kilohm resistor. System side: This signal is output by CPLD over the drive circuit and pulled up to 3.3 V by a 4.7-kilohm resistor.
IP PRESENT# = High	The PSU does not act.
IP PRESENT# = Low	The PSU enters the SV12 output mode.

Table 10-14 IP PRESENT output

IP PRESENT Output	Min.	Max.
Low level voltage	0 V	0.6 V
High level voltage	2.0 V	3.465 V
Sink current, IP PRESENT = Low	-	4 mA
Source current, IP PRESENT = High	-	4 mA

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IP PRESENT Output	Min.	Max.
Signal rise and fall time	-	200 µs

10.10 CYC_PWR#

The CYC_PWR# signal is sent to PSUs by the system to shut down or restore the MV12 main output of the PSU.

- The signal is valid if the CYC_PWR# signal is at low level for 100–150 ms. The PSU shuts down after the delay time (configurable) and then starts after the restart time (configurable).
- The CYC_PWR# signal is invalid if both the OPOK and CYC_PWR# signals are low level.
- The signal is invalid if the CYC_PWR# signal is at low level for less than 100 ms.
- The CYC_PWR# signal is invalid when a PSU is during delay shutdown or restarting. The CYC_PWR# signal is valid after the OPOK signal becomes high level.

Table 10-15 CYC_PWR# signal characteristics

Signal Characteristics	
Signal type	PSU side: This signal is pulled up to the 3.3 V auxiliary power source inside the PSU by a 4.75-kilohm resistor. System side: This signal is output by CPLD over the drive circuit and pulled up to 3.3 V by resistor greater than 4.7 kilohm.
CYC_PWR# = High	The PSU does not act.
CYC_PWR# = Low	The PSU enters power supply cycle power mode.

Table 10-16 CYC_PWR# output

CYC_PWR# Output	Min.	Max.
Low level voltage	0 V	0.8 V
High level voltage	2.0 V	3.465 V
Sink current, CYC_PWR# = Low	-	4 mA
Source current, CYC_PWR# = High	-	4 mA
Signal rise and fall time	-	200 µs

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10.11 I-MON

This signal is a 12 V current sharing signal and is connected to the system backplane.

PSU side: Internal current sharing bus

System side: The backplane directly connects the I-MON signals of all PSUs together.

10.12 PS_INTERRUPT

The PS_INTERRUPT# signal indicates PSU I2C alarm interruption.

Table 10-17 PS_INTERRUPT# signal characteristics

Signal Characteristics	
Signal type	PSU side: Power is output over the OD, and there is no pull-up on the PSU side. System side: Pulled up to 3.3 V level and then reversely sent to the system over the MOSFET transistor.
PS_INTERRUPT = High	The PSU is normal.
PS_INTERRUPT = Low	The PSU generates an alarm.

10.13 EFUSEV

The system samples the input current of the main service load branch (12 V), converts the current to a linear voltage analog signal, and sends the signal to the EFUSEV pin of the PSU. If the value of EFUSEV is greater than 1.0 V, the main service of the system is working. In this case, the PSU cannot enable deep sleep and automatic PFC shutdown. If the value of EFUSEV is less than 0.8 V, the main service of the system is powered off or does not work. In this case, the PSU can enable deep sleep and automatic PFC shutdown.

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Table 10-18 EFUSEV signal characteristics

Signal Characteristics	
Signal type	PSU side: Send to the PSU DSP by a 100-ohm series resistor. System side: ● If this function is enabled, the system sends the voltage analog signal of the main service circuit to the PSU, or CPLD outputs this signal over the drive circuit and this signal is pulled up to 3.3 V level by a 4.7-kilohm resistor. ● If this function is disabled, this signal is pulled up to 3.3 V level by a 4.7-kilohm resistor.
EFUSEV = High	Disable deep sleep and automatic PFC shutdown.
EFUSEV = Low	Enable deep sleep and automatic PFC shutdown.

Table 10-19 EFUSEV output

EFUSEV Output	Min.	Max.
Low level voltage	0 V	0.8 V
High level voltage	1.0 V	3.465 V

10.14 SMART_ON

The SMART_ON signal is used to wake up a cold-standby PSU (low level by default).

- Set an active PSU: The system sets one of the PSUs to be the active PSU (by sending the **D0h 0x01** command). The active PSU sets the SMART_ON signal to high level.
- Set a standby PSU: The system sends a standby PSU command (**D0h 0x02/0x03/0x04**). The standby PSU enters cold standby mode. (Two conditions that the standby PSU enter the cold standby mode must be met. 1. The SMART_ON signal is at high level. 2. The system sends the **D0h 0x02/0x03/0x04** command.)
- Wake up the standby PSU: When the host is faulty, the active PSU sets the SMART_ON signal to low level to wake up the standby PSU.

NOTE

The mixed insertion of this signal will cause other PSUs to work abnormally.

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Table 10-20 SMART_ON signal characteristics

Signal Characteristics	
Signal type	System side: The SMART_ON signals of all PSU backplanes are connected together through the cables.
SMART_ON = High	The PSU enters cold standby mode.
SMART_ON = Low	The PSU exits from cold standby mode.

Table 10-21 SMART_ON output

SMART_ON Output	**Min.**	**Max.**
Low level voltage	0 V	0.8 V
High level voltage	2.0 V	3.6 V

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11 Communication

11.1 Signal Characteristics
11.2 Data Link Layer Protocol
11.3 Network Layer Protocol
11.4 Application Layer Protocol
11.5 PSU Reverse Power Supply

11.1 Signal Characteristics

Table 11-1 PMBus detection precision

Parameter	Min.	Typ.	Max.	Unit	Notes & Conditions
Input voltage detection precision	–2	-	2	%	MV12, T_A = 25°C-35°C (77°F-95°F)
Input current detection precision	–0.25	-	0.25	A	V_{in} = 230 V AC/270 V DC, T_A = 25°C-35°C (77°F-95°F), I_{in} ≤ 0.5 A
	–0.1	-	0.1	A	V_{in} = 230 V AC/270 V DC, T_A = 25°C-35°C (77°F-95°F), 0.5 A < I_{in} ≤ 2 A
	-	5	-	%	V_{in} = 230 V AC/270 V DC, T_A = 25°C-35°C (77°F-95°F), 2 A < I_{in} ≤ 5 A
	-	3	-	%	V_{in} = 230 V AC/270 V DC, T_A = 25°C-35°C (77°F-95°F), I_{in} > 5 A
Input power	-	20	-	W	V_{in} = 115 V AC/230 V AC/270 V DC, MV12, T_A = 25°C-35°C (77°F-95°F), P_{in} ≤ 20 W

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Parameter	Min.	Typ.	Max.	Unit	Notes & Conditions
detection precision	–10	-	10	W	V_{in} = 115 V AC/230 V AC/270 V DC, MV12, T_A = 25°C-35°C (77°F-95°F), 20 W < P_{in} ≤ 100 W
	–5	-	5	%	V_{in} = 115 V AC/230 V AC/270 V DC, MV12, T_A = 25°C-35°C (77°F-95°F), 100 W < P_{in} ≤ 300 W
	–3	-	3	%	V_{in} = 115 V AC/230 V AC/270 V DC, MV12, T_A = 25°C-35°C (77°F-95°F), P_{in} > 300 W
Output voltage detection precision	–2	-	2	%	MV12, T_A = 25°C-35°C (77°F-95°F)
Output current detection precision	–1	-	1	A	Minimum load > 2 A, MV12, T_A = 25°C-35°C (77°F-95°F); I_{out} ≤ 3 A
	–20	-	20	%	Minimum load >2 A, MV12, T_A = 25°C-35°C (77°F-95°F); 3 A < I_{out} ≤ 5 A
	–10	-	10	%	Minimum load > 2 A, MV12, T_A = 25°C-35°C (77°F-95°F); 5 A < I_{out} ≤ 8 A
	–5	-	5	%	Minimum load > 2 A, MV12, T_A = 25°C-35°C (77°F-95°F); 8 A < I_{out} ≤ 15 A
	–3	-	3	%	Minimum load > 2 A, MV12, T_A = 25°C-35°C (77°F-95°F); I_{out} > 15 A
Output power detection precision	–10	-	10	W	Minimum load > 2 A, V_{in} = 230 V AC/ 270 V DC, MV12, T_A = 25°C-35°C (77°F-95°F); P_{out} ≤ 100 W
	–5	-	5	%	Minimum load > 2 A, V_{in} = 230 V AC/ 270 V DC, MV12, T_A = 25°C-35°C (77°F-95°F); P_{out} > 100 W
Temperature detection precision	–5	-	5	ºC	T_A = 25°C (77°F)
Wind speed detection precision	–5	-	5	%	-

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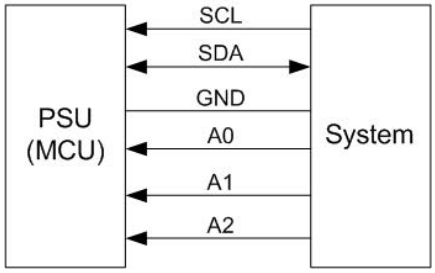
11.2 Data Link Layer Protocol

The link layer uses the PMBus V1.2 protocol and complies with *PMBus_Specification_Part_I_Rev_1-2_20100906* and *PMBus_Specification_Part_II_Rev_1-2_20100906*.

11.2.1 I2C Signal

The PSU includes a main control unit (MCU) used for monitoring and an electrically erasable programmable read only memory (EEPROM) of 256 bytes used for storing fault records. The PSU can use the EEPROM in the MCU to store fault records. The system sends commands to the MCU over the I2C bus to read fault records from the PSU. **Figure 11-1** shows the I2C interface communication diagram.

Figure 11-1 I2C interface



SCL and SDA are the serial clock signal and the serial data signal of the I2C interface.

PSU side:	System side:
• SCL and SDA must be pulled up to 3.3 V by a 20-kilohm resistor. • The voltage source must be generated by the auxiliary power source and system-side 12 V bus that supplies power to the secondary MCU at the same time. • When power is supplied from the system side, the fuse or overcurrent/ short-circuit protection circuit must be used. • The capacitance on the SCL and SDA in the PSU cannot exceed 66 pF.	The voltage of SCL and SDA is pulled up to 3.3 V by increasing the resistance by 3-10 kilohm.

11.2.2 I2C Address

Addresses A2, A1, and A0 are allocated to a PSU.

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PSU side:

- Interconnection in the PSU: A2, A1, and A0 must be pulled up to the internal voltage source through a 10-kilohm (reference value) resistor. The voltage source must be the voltage source that supplies power to the secondary MCU. The voltage source is supplied by the internal auxiliary power source, which is generated by the PSU and system-side 12 V bus at the same time.
- When power is supplied from the system side, the fuse or overcurrent/short-circuit protection circuit must be used.

System side:

If this signal is connected to GND through a 300-ohm resistor, the address bit is 0. If this signal is left open, the address bit is 1.

The I2C addresses of the PSU from high to low are A2, A1, and A0. See **Table 11-2** for details.

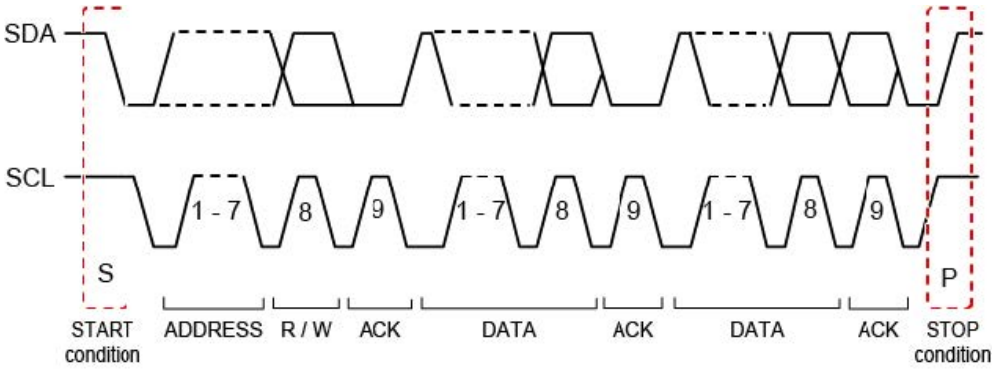
Table 11-2 I2C address

PSU A2/A1/A0	0/0/0	0/0/1	0/1/0	0/1/1	1/0/0	1/0/1	1/1/0	1/1/1
MCU	0xB0	0xB2	0xB4	0xB6	0xB8	0xBA	0xBC	0xBE

11.2.3 Data Transmission Mode

The I2C transmission standard is used. Data transmission rate is 100 Kbit/s. The timing definition of lower-layer signals, such as START, STOP, R/W, ADDRESS, ACK/NACK, bus arbitration, clock synchronization, and clock extension (except bus timeout), complies with PSMI. See **Figure 11-2** for the I2C data transmission mode.

Figure 11-2 I2C data transmission mode



11.2.4 Bus Timeout

The I2C program of the PSU is reset to 0 if the PSU detects that the SCL or SDA data line is held low for more than 300 ms.

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11.3 Network Layer Protocol

11.3.1 Secondary Addressing Method

The PSU serves as the secondary device, and the PSU address is identified by the hardware and assigned in static mode. The primary device accesses secondary devices independently based on the secondary device addresses determined by the hardware

11.3.2 Data Packet Verification Mode

To ensure data integrity and accuracy during communication, the PSU uses the 8-bit CRC checksum mechanism.

The last byte of each sent and received communication is the CRC code of this signal.

- When the primary device sends data to a secondary device, PEC (CRC) contains the address byte, read/write bit, command byte, and multiple pieces of data to be sent.
- The primary device obtains data from a secondary device: PEC (CRC) sent by the secondary device contains the address bytes obtained by the secondary device, read/write bits, command codes, and data bytes sent by the secondary device.

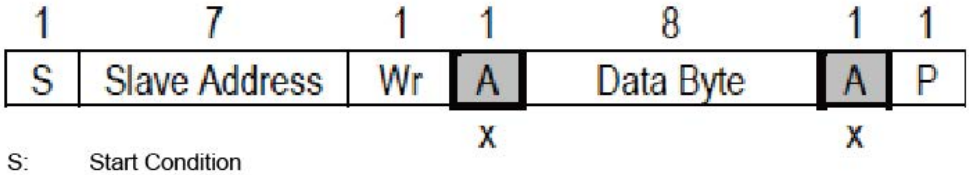


11.3.3 CRC Generation Rules

The generator polynomial is $x^8 + x^2 + x^1 + 1$, and the CRC code is 100000111.

11.3.4 Data Transmission

PMBus communication supports 10 data formats in total. All data formats adopt PEC verification.



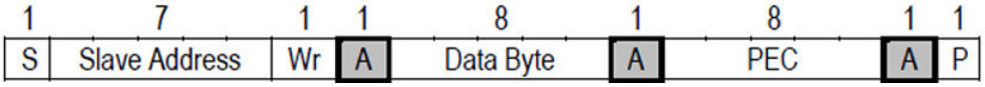
S: Start Condition
Sr: Repeated start condition
Rd: Read (bit value of 1)
Wr: Write (bit value of 0)
x: Shown under a field indicates that field is required to have the value of 'x'
A: Acknowledge (this bit position may be '0' for an ACK or '1' for a NACK)
P: Stop condition
PEC: Packet Error Code
...: Continuation of protocol

Master-to-Slave

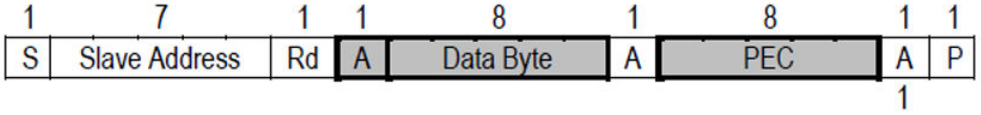
Slave-to-Master

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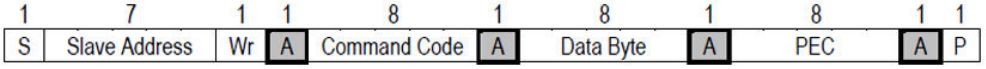
- Send Byte command and response.



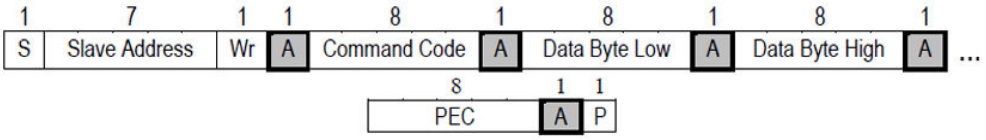
- Receive Byte command and response.



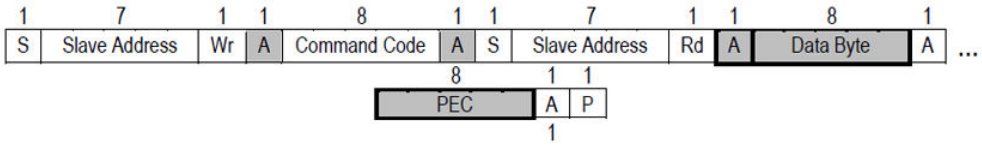
- Write Byte command and response.



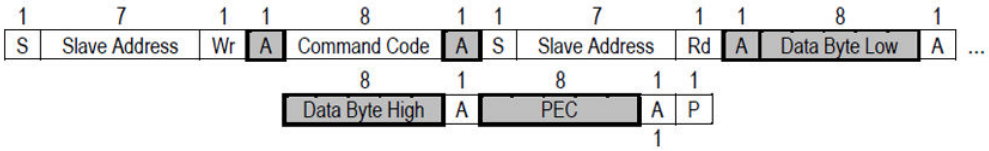
- Write Word command and response.



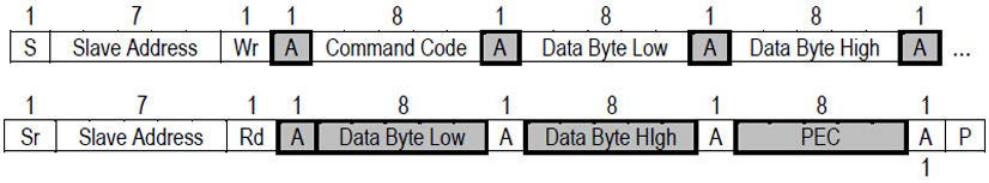
- Read Byte command and response.



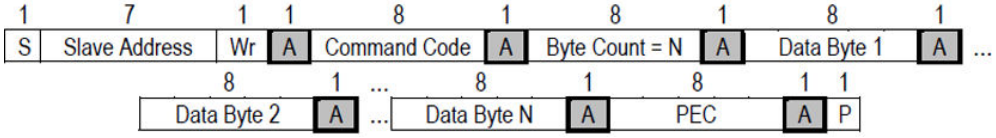
- Read Word command and response.



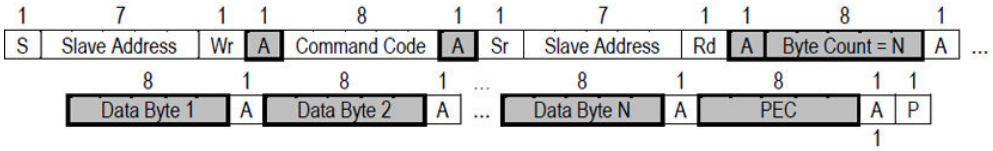
- Process Call command and response.



- Block Write command and response.

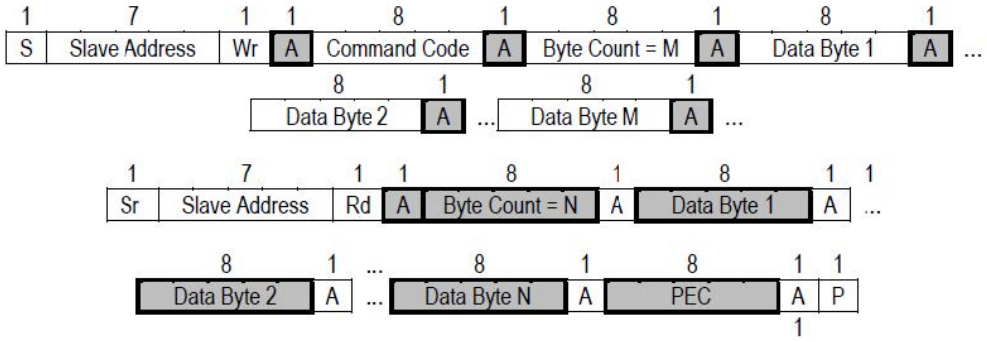


- Block Read command and response.



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- Block Read – Block Write Process Call command and response.



11.4 Application Layer Protocol

NOTE

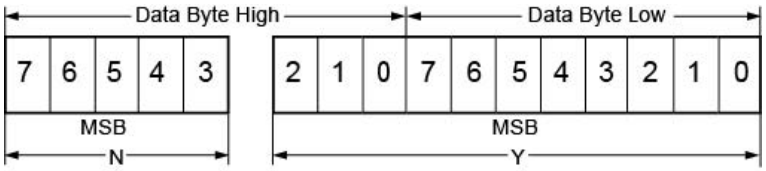
- The PMBus alarm is not latched. If a PSU is faulty, an alarm is reported. After the PSU fault is rectified, the alarm is automatically cleared.
- If the PSU is faulty, the system sends **CLEAR_FAULTS** to clear the alarm. If the alarm persists, the PSU continues to report the alarm.
- The **FAULT** alarm and the **WARNING** alarm are mutually exclusive.

11.4.1 Data Format

Linear 11 Data Format

The Linear 11 data format is a two-byte value with an 11-bit binary signed mantissa (two's complement) and a 5-bit binary signed exponent (two's complement), as shown in **Figure 1 Linear 11 data format**.

Figure 11-3 Linear 11 data format



The relationship between N, Y, and actual value X is given by the following equation:

$X = Y \times 2^N$

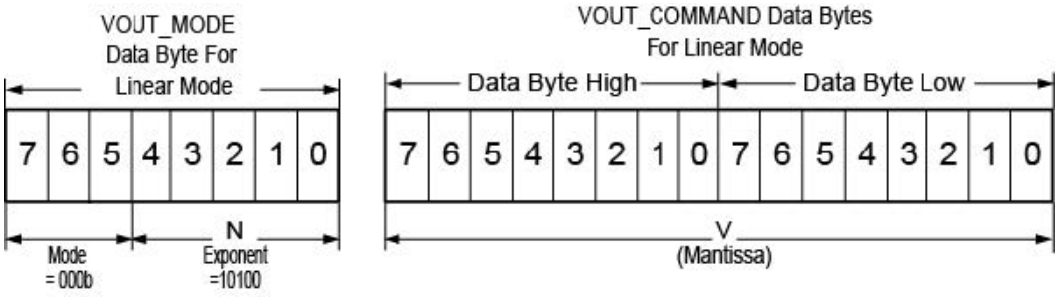
- Where: Y is the 11-bit, binary signed mantissa (two's complement).
- N is the 5-bit, binary signed exponent (two's complement).

Linear 16 Data Format

The Linear 16 data format is a two-byte value with a 16-bit binary unsigned mantissa and a 5-bit binary signed exponent (two's complement), as shown in **Figure 11-4**.

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Figure 11-4 Linear 16 data format



The output voltage is calculated as follows:

Voltage = V x 2^N

- Where: Voltage is the output voltage value.
- V is the 16-bit unsigned integer.
- N is the 5-bit signed integer (two's complement), and N is –12.

11.4.2 Commands

Commands

Command ID	Command Name	Data Type	Data Format	Description
0x01	PMBUS_CMD_OPERATION	Read/ Write byte	HEX	Remote startup and shutdown ● 0x80: Start the PSU. ● 0x40: Soft shutdown. The PSOK time sequence meets the requirements. ● 0x00: Reset the PSU.
0x03	PMBUS_CMD_CLEAR_FAULTS	Write byte	HEX	Clearing faults; No parameter available; Sending the command with CRC and clearing the fault alarms after receiving the command.
0x20	PMBUS_CMD_VOUT_MODE	Read byte	HEX	Output mode
0x21	PMBUS_CMD_VOUT_CTRL	Read/ Write word	LINEAR 16 Q12	Output voltage configuration. In non-equipment mode, the voltage range must be 11.97 V to–12.35 V.
0x31	PMBUS_CMD_RATED_POUT	Read word	LINEAR 11	Maximum output power

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Command ID	Command Name	Data Type	Data Format	Description
0x3A	PMBUS_CMD_FAN_CONFIG	Read/ Write byte	HEX	Fan configuration; default value: 0xD0
0x3B	PMBUS_CMD_FAN_COMMAND	Read/ Write word	HEX	Fan speed setting
0x78	PMBUS_CMD_STATUS_BYTE	Read/ Write byte	HEX	Status single-byte
0x79	PMBUS_CMD_STATUS_WORD	Read/ Write word	HEX	Status dual-byte
0x7A	PMBUS_CMD_STATUS_VOUT	Read/ Write byte	HEX	Output voltage status
0x7B	PMBUS_CMD_STATUS_IOUT	Read/ Write byte	HEX	Output current status
0x7C	PMBUS_CMD_STATUS_INPUT	Read/ Write byte	HEX	Input status
0x7D	PMBUS_CMD_STATUS_TEMP	Read/ Write byte	HEX	Temperature status
0x7E	PMBUS_CMD_STATUS_CML	Read/ Write byte	HEX	CMD status
0x80	PMBUS_CMD_STATUS_MFR_SPECIFIC	Read/ Write byte	HEX	MFR status
0x81	PMBUS_CMD_STATUS_FANS	Read/ Write byte	HEX	Fan status
0x88	PMBUS_CMD_READ_VIN	Read word	LINEAR 11	Input voltage
0x89	PMBUS_CMD_READ_IIN	Read word	LINEAR 11	Input current
0x8B	PMBUS_CMD_READ_VOUT	Read word	LINEAR 16	Output voltage
0x8C	PMBUS_CMD_READ_IOUT	Read word	LINEAR 11	Output current
0x8D	PMBUS_CMD_READ_TEMPERATURE_1	Read word	LINEAR 11	Ambient temperature

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Command ID	Command Name	Data Type	Data Format	Description
0x8E	PMBUS_CMD_READ_TEMPERATURE_2	Read word	LINEAR 11	Primary side temperature (MOS1)
0x8F	PMBUS_CMD_READ_TEMPERATURE_3	Read word	LINEAR 11	Secondary side temperature (MOS1)
0x90	PMBUS_CMD_READ_FAN_SPEED	Read word	DEC	Fan speed
0x95	PMBUS_CMD_READ_FREQUENCY	Read word	LINEAR 11	PSU frequency
0x96	PMBUS_CMD_READ_POUT	Read word	LINEAR 11	Output power
0x97	PMBUS_CMD_READ_PIN	Read word	LINEAR 11	Input power
0x98	PMBUS_CMD_PMBUS_REVISION	Read byte	DEC	PMBus version
0x99	PMBUS_CMD_MFR_ID	Read block	ASCII	Vendor name
0x9A	PMBUS_CMD_MFR_MODEL	Read/ Write block	ASCII	PSU name
0x9B	PMBUS_CMD_HARD_VERSION	Read block	ASCII	Hardware version
0x9D	PMBUS_CMD_MFR_DATE	Read/ Write block	ASCII	Production date
0x9E	PMBUS_CMD_MFR_SERIAL	Read/ Write block	ASCII	Product SN
0xBD	PMBUS_CMD_E_LABLE_LENGTH	Read/ Write block	HEX	Length and frame length of the e-label
0xBE	PMBUS_CMD_E_LABLE_INDEX	Read/ Write word	DEC	E-label number

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Command ID	Command Name	Data Type	Data Format	Description
0xBF	PMBUS_CMD_E_LABLE_CONTENT	Read/ Write block	ASCII	E-label content
0xC0	PMBUS_CMD_POWER_CYCLE_TIME	Read/ Write word	DEC	Shutdown delay time ● Bits F–8: PSU shutdown delay time ● Bits 7–0: PSU shutdown time
0xC6	PMBUS_CMD_WARNING_COUNT1	Read/ Write word	DEC	Alarm counter 1; Writing 0x0000 clears the alarm and recounts the alarm. Writing other values does not take effect.
0xC7	PMBUS_CMD_WARNING_COUNT2	Read/ Write word	DEC	Alarm counter 2; Writing 0x0000 clears the alarm and recounts the alarm. Writing other values does not take effect.
0xC8	PMBUS_CMD_WARNING_COUNT3	Read/ Write word	DEC	Alarm counter 3; Writing 0x0000 clears the alarm and recounts the alarm. Writing other values does not take effect.
0xCC	PMBUS_CMD_MAX_VIN	Read/ Write word	LINEAR 11	Maximum input voltage; Writing 0x0000 clears the alarm, writing other values does not take effect.
0xCD	PMBUS_CMD_MAX_IIN	Read/ Write word	LINEAR 11	Maximum input current; Writing 0x0000 clears the alarm, writing other values does not take effect.
0xCE	PMBUS_CMD_PS_CONTROL	Read/ Write word	HEX	PSU control register; default value: 0x0000

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Command ID	Command Name	Data Type	Data Format	Description
0xCF	PMBUS_CMD_READ_POWER_TYPE	Read word	HEX	PSU types Currently, the PSU always reports 3, that is, AC & 240HVDC. The meanings of the data are as follows: ● 0: AC ● 1: 48 V DC ● 2: HVDC ● 3: AC & 240HVDC ● 4: AC & 240HVDC & 380HVDC
0xD0	PMBUS_CMD_SMART_ON_CONFIG	Read/ Write byte	HEX	Cold standby configuration command
0xD4	PMBUS_CMD_SET_PSALERT	Read/ Write byte	HEX	Forcible alarm command. ● Write 0x20 to forcibly interrupt the signal alarm. ● Write 0x00 to clear the software control and restore the PSU normal control. ● Writing other values is invalid.
0xD6	PMBUS_CMD_MAX_PIN	Read/ Write word	LINEAR 11	Maximum input power
0xD7	PMBUS_CMD_MAX_POUT	Read/ Write word	LINEAR 11	Maximum output power
0xD8	PMBUS_CMD_MAX_TEMP_MONITOR	Read/ Write word	LINEAR 11	Maximum temperature of the PSU air intake vent
0xDB	PMBUS_CMD_MAX_12V_IOUT	Read/ Write word	LINEAR 11	Maximum output current
0xDE	PMBUS_CMD_READ_PART_NUMBER	Read/ Write block	ASCII	xFusion number

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Command ID	Command Name	Data Type	Data Format	Description
0xDF	PMBUS_CMD_READ_INPUT_TYPE	Read word	HEX	Input voltage type PSU real-time input status: ● 0: no input or input voltage abnormal ● 1: AC input ● 2: HVDC ● 3: low-voltage DC (no such input is available)
0xE0	PMBUS_CMD_WRITE_EVENT_INDEX	Write byte	DEC	Black box No.
0xE0	PMBUS_CMD_READ_EVENT_LOG	Read block	HEX	Black box content
0xE1	PMBUS_CMD_WRITE_EVENT_LOG	Write word	HEX	Writing logs
0xE4	PMBUS_CMD_DCDC_SOFT_VER	Read word	HEX	DCDC software version
0xE7	PMBUS_CMD_PFC_SOFT_VER	Read word	HEX	DCDC software version
0xED	PMBUS_CMD_LED_CTRL	Read/ Write byte	HEX	LED control If bit 0 and bit 1 are written to 1 at the same time, the command is invalid. ● Bit 7: 1 indicates active PSU control (the indicator is valid), and 0 indicates PSU automatic control. ● Bit 1: 1 indicates that the orange LED indicator is on, and 0 indicates that it is off. ● Bit 0: 1 indicates that the green LED indicator is on, and 0 indicates that it is off.
0xEE	PMBUS_CMD_LOAD_SHARE_SCALE	Read/ Write byte	DEC	Coefficient of proportionality for current sharing, default value: 1. Value range: 1 to 8. For example, 1 indicates 1:1 and 4 indicates 1:4.

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Command ID	Command Name	Data Type	Data Format	Description
0xFA	PMBUS_CMD_UPDATE_TIME	Write block	TIME	Time serving
0xFB	PMBUS_CMD_LOAD_PARAM	Read block	HEX	Load parameters
0xFC	PMBUS_CMD_LOAD_START	Read/ Write word	HEX	Load commands
0xFD	PMBUS_CMD_LOAD_DATA	Write block	HEX	Load data

11.4.3 Command Descriptions

PMBUS_CMD_STATUS_BYTE (0x78)

Bit Address	Command Name
7	Reserved
6	Reserved
5	VOUT_OV_FAULT
4	IOUT_OC_FAULT
3	VIN_UV_FAULT
2	TEMPERATURE
1	CML
0	Reserved

PMBUS_CMD_STATUS_WORD (0x79)

Bit Address	Command Name
(Low) 7	Reserved
6	Reserved
5	VOUT_OV_FAULT
4	IOUT_OC_FAULT

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Bit Address	Command Name
3	VIN_UV_FAULT
2	TEMPERATURE
1	CML
0	Reserved
(High) 7	VOUT
6	IOUT/POUT
5	INPUT
4	MFR_SPECIFIC
3	Reserved
2	FANS
1	Reserved
0	Reserved

PMBUS_CMD_STATUS_VOUT (0x7A)

Bit Address	Command Name
7	VOUT_OV_FAULT
6	VOUT_OV_WARNING
5	VOUT_UV_WARNING
4	VOUT_UV_FAULT
3	Reserved
2	Reserved
1	Reserved
0	Reserved

PMBUS_CMD_STATUS_IOUT (0x7B)

Bit Address	Command Name
7	IOUT_OC_FAULT
6	Reserved
5	IOUT_OC_WARNING

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Bit Address	Command Name
4	Reserved
3	Reserved
2	Reserved
1	Reserved
0	Reserved

PMBUS_CMD_STATUS_ INPUT (0x7C)

Bit Address	Command Name
7	VIN_OV_FAULT
6	VIN_OV_WARNING
5	VIN_UV_WARNING
4	VIN_UV_FAULT
3	Reserved
2	Reserved
1	Reserved
0	Reserved

PMBUS_CMD_STATUS_TEMP (0x7D)

Bit Address	Command Name
7	OT_FAULT
6	OT_WARNING
5	Reserved
4	Reserved
3	Reserved
2	Reserved
1	Reserved
0	Reserved

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PMBUS_CMD_STATUS_FANS (0x81)

Bit Address	Command Name
7	Fan 1 Fault
6	Reserved
5	Fan 1 Warning
4	Reserved
3	Fan 1 Speed Override
2	Reserved
1	Reserved
0	Reserved

PMBUS_CMD_STATUS_CML (0x7E)

Bit Address	Command Name
7	Reserved
6	Reserved
5	Packet Error Check Failed
4	Memory Fault Detected
3	Processor Fault Detected
2	Reserved
1	Other Communication Fault
0	Reserved

PMBUS_CMD_MFR_SPECIFIC (0x80)

Bit Address	Command Name
7	PSON12V#
6	INSTALLED#
5	IPOK
4	OPOK
3	OPERATION OFF
2	SET Interrupt

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Bit Address	Command Name
1	PFC ENABLED
0	PFC BUS UV

11.4.4 Online Upgrade

Both the primary and secondary sides of the PSU support online upgrade.

11.4.5 Black Box Function

The PSU has 440-byte space for storing fault logs. The PSU provides a read channel for external systems by the **Read Event Log** command.

11.4.5.1 Runtime Counter

The total runtime is the sum of the time when the PSU works properly (OPOK is normal), is presented in the unit of seconds, and occupies four bytes.

11.4.5.2 Event Log Update

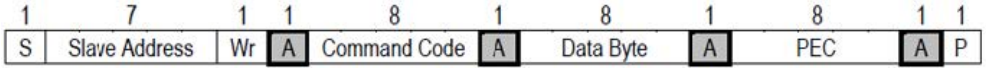
The PSU maintains a circular event log of the last ten records. Before saving the event log, the PSU shall set the event update flag in the event log to 1. Each log shall start with an event number followed by a 4-byte runtime (measured in seconds). The total runtime must be updated before the log is saved.

The PSU abnormality alarm status, input voltage, input current, output voltage, output current, temperature, fan speed, maximum input voltage, maximum output current, PS control, and other information will be stored in the fault record register. No output due to the hot swap, the switch PSU output operation and the **CYC_PWR** issued by the system cannot be saved by the PSU fault record function. Faults including input power failure, input overvoltage, input undervoltage, output overvoltage, output overcurrent/short-circuit, overtemperature, fan fault, and PSU damage will be stored. If a new fault is the same as the latest record in the event log, this fault will not be stored. That is, two consecutive same faults cannot exist in the fault record.

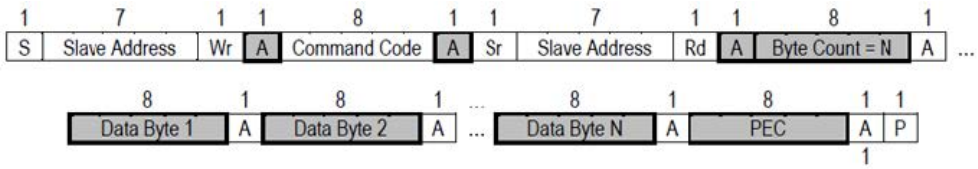
The runtime counter shall be updated every time an event occurs regardless of whether the event is written into the event log. When there are more than 10 events, the 11th event shall overwrite the 1st event, the 12th event shall overwrite the 2nd event, and so on.

11.4.5.3 Format of Reading Logs

- Write Byte Index (0–10)



- Read Block (N = 40)



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11.5 PSU Reverse Power Supply

- In case of parallel PSUs, if the system works normally and there is no input from a single PSU, the system must be able to communicate with the PSU normally.
- During the PSU reverse power supply, the power indicator is off, which is the same as that in the case of no input.
- During the PSU reverse power supply, the code upgrade is supported on the DCDC side.

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12 Heat Dissipation Requirements and Fan Control

The PSU supports forcible air cooling for heat dissipation. The PSU uses 40 mm (W) x 40 mm (H) fans. The air blows in from the rear and blows out from the front.

Heat Dissipation Requirements

The fan should meet the following requirements:

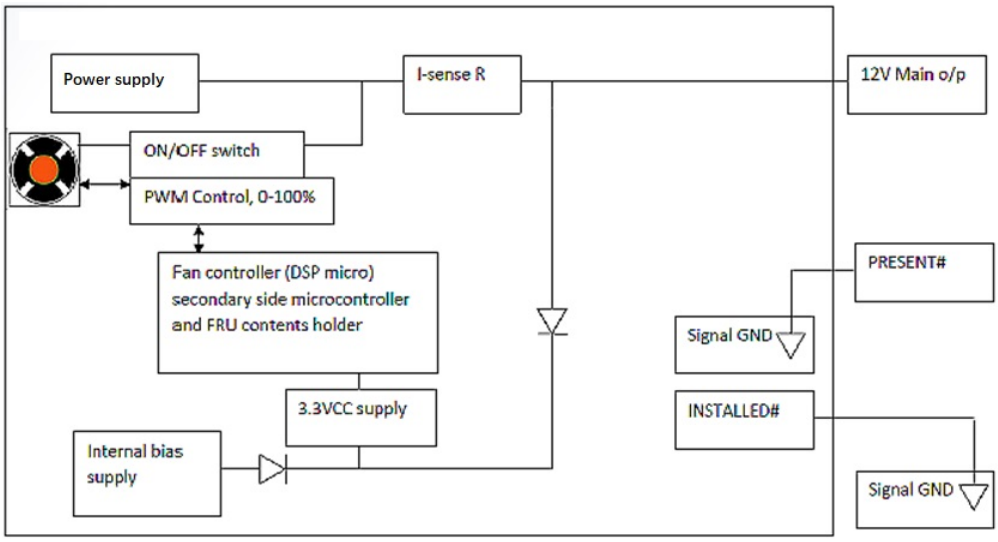
- The fan cannot stop rotating in the case of reverse power supply.
- Before overtemperature protection, the fan must operate at full speed.
- When the temperature at the air intake vent of the PSU reaches 45°C (113°F), the fan speed can be adjusted.

Fan Power Supply Mode

- The fan obtains power from the output bus. A fan fault cannot cause a system alarm or reset. The bus voltage must be greater than or equal to 11 V and the time when the bus voltage is lower than 11.4 must not exceed 100 μs.
- The fan should be isolated if a fault occurs.
- The system can deliver a command to turn off the fan.

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Figure 12-1 Fan power supply circuit



Noise Requirements

Air Intake Vent Temperature	Load	Maximum Value	Test Method
25°C (77°F)	40%	50 dB (sound pressure level)	The test should be conducted at 1 m away from the PSU. The noise sensor should face the air exhaust vent of the fan.

Fan Protection

- If PSUs are connected in parallel and one of the fans or the power supply circuit is short-circuited or open-circuited, the 12 V and 6 V routes of the system backplane should not exceed the normal dynamic change voltage restriction specifications. The system operation should not be affected.
- The fan fault detection circuit can report a fault only when it detects a fan or fan circuit fault and the fault lasts for over 20s.

NOTE

Long-time fan operation ages the internal bearing, which prolongs fan startup time. The fan is considered to be faulty only when it does not reach the specified rotational speed after more than 20s.

- A fan fault can be rectified.

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13 Indicator on the Panel

The power supply of the PSU indicator is the same as that of the secondary MCU, as shown in the **Figure 13-1** below.

Figure 13-1 Position of the indicator on the panel.

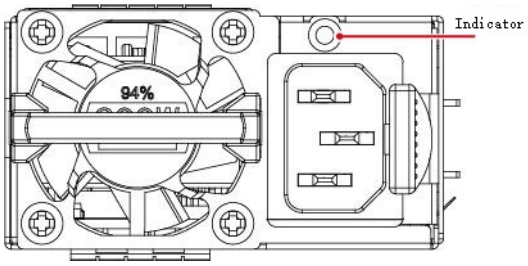


Table 13-1 Indicator on the panel

Indicator	Status	Description
Power status indicator	Steady green	The input and mains MV12 output are normal.
	Blinking green at 1 Hz	• The input is normal, and the PSU shuts down MV12 output due to the INSTALLED# signal. • The input is normal. The PSU enters MV6 mode because the PSON12V# signal is at high level. • Input undervoltage occurs. • The standby PSU is in cold standby mode. • The PSU enters deep sleep mode. • The input is normal, and the PSU enters SV12 mode because the IP PRESENT signal is at low level.
	Steady orange	The input is normal. There is no output due to overtemperature protection, output overcurrent/short circuit, output overvoltage, short-circuit protection, or component failure (excluding failure of all components).

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Indicator	Status	Description
	Off	No input.
	Blinking green at 4 Hz	Online upgrade is in progress.

Table 13-2 Panel indicator luminance

Indicator	Meaning	Luminance Range
Green	Normal	> 36cd/m^2
Orange	Normal	> 36cd/m^2

NOTE

- For normal startup, the green indicator should be on and should not blink. For normal poweroff, the power indicator should be off and should not be steady red or blinking red.
- The position of the LED indicator should meet the ESD requirements.
- The indicator uses the Φ3 mm bi-color indicator.
- An I2C communication fault is not indicated by the indicator status.
- When the indicator is controlled by the main control unit, the indicator status depends on the main control unit command.

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14 Product Storage, and Transportation

Storage Requirements

The product must be stored in a dry and well-ventilated warehouse where the temperature ranges from –40°C to +85°C (-40°F to +185°F), the relative humidity is not greater than 80%, and no corrosive gas exists.

Transportation Requirements

During transportation, the product must be securely placed in a packing case. The packing case must comply with related international standards and be printed with marks such as "Handle with care" and "Keep dry". The packing case containing the product can be transported by any means. The packing case must be protected against rains, snows, and mechanical impact during transportation.

Enclosure Protection

Table 14-1 Enclosure protection level

Item	Requirement
Enclosure protection level	IP20 (normal maintenance surface)

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A Appendix

A.1 EMC Requirements

Parameter	Condition	Criterion
Conducted emission (CE)	6 dB	EN 55032, class A
Radiated emission (RE)	6 dB	EN 55032, class A
	6 dB, 30 MHz–1 GHz	FCC Part15, class A
ESD	Contact: 6 kV, air: 8 kV	IEC 61000-4-2, criterion B
	Contact: 8 kV, air: 15 kV	IEC 61000-4-2, criterion C
EFT	±2 kV	IEC 61000-4-4, criterion B
RS	10 V/m, 80 MHz–6 GHz	IEC 61000-4-3, criterion A
CS	10 V, 150 kHz–80 MHz	IEC 61000-4-6, criterion A

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Parameter	Condition	Criterion
Power magnetic susceptibility (PMS)	30 A/m	IEC 61000-4-8: 2001, criterion A
Surge	AC input port: ● Differential mode: ±2.5 kV for L to N (1.2/50 μs, 2 ohms) ● Common mode: ±2.5 kV for L to PE, N to PE, and L and N to PE (1.2/50 μs, 12 ohms)	IEC 61000-4-5, criterion B
	HVDC input port: ● Differential mode: ±2 kV for P to N (1.2/50 μs, 2 ohms) ● Common mode: ±2 kV for P to PE, N to PE, and P and N to PE (1.2/50 μs, 12 ohms)	
Voltage fluctuation and flicker	Voltage fluctuation and flicker limit of class A products	IEC 61000-3-3
Current harmonics emission	Harmonic current limit of class A products	IEC 61000-3-2
Dip (AC)	Dip to 0% UT, single PSU 100% load, hold-up time: 10 ms	IEC 61000-4-11, criterion A
	Dip to 0% UT, single PSU 50% load, dual PSUs 100% load, hold-up time: 20 ms	IEC 61000-4-11, criterion A
	Dip to 70% UT, hold-up time: 500 ms	IEC 61000-4-11, criterion B
	Dip to 0% UT, hold-up time: 5000 ms	IEC 61000-4-11, criterion C
Dip (HVDC)	Dip to 40% UT, hold-up time: 1 ms/3 ms/10 ms/30 ms/100 ms/300 ms/1000 ms	IEC 61000-4-11, criterion B
	Dip to 70% UT, hold-up time: 1 ms/3 ms/10 ms/30 ms/100 ms/300 ms/1000 ms	IEC 61000-4-11, criterion B
	Dip to 0% UT, hold-up time: 1 ms/3 ms/10 ms/30 ms/100 ms/300 ms/1000 ms	IEC 61000-4-11, criterion B

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Parameter	Condition	Criterion
	Dip to 80% UT, hold-up time: 100 ms/300 ms/1000 ms/ 3000 ms/10,000 ms	IEC 61000-4-11, criterion A
	Dip to 120% UT, hold-up time: 100 ms/300 ms/1000 ms/ 3000 ms/10,000 ms	IEC 61000-4-11, criterion A
Criterion A: The performance is normal within the technical requirements. (Normal output voltage: The output voltage is greater than 11.0 V and no alarm is generated by the alarm signal.) Criterion B: The PSU passes the test with the server. During the test, the PSU output voltage should be kept within the normal range, no alarm is generated by the alarm signal, and the criterion module B of DC dip can be restarted. Criterion C: Automatic reset of short-term function interruption is allowed, but long-term function interruption or manual reset is not allowed.		

A.2 Product Safety Testing

Dielectric Strength Testing

Test Item	Minimum Test Voltage	Test Time	Leakage Current	Arching Determination
Input and PE	2500 V DC	1 min	≤ 10 mA	No breakdown or arcing.
Output and PE	GND of the 12 V output and the PE are short-circuited inside the PSU.			

NOTE

Input to output must meet the reinforced insulation requirements after the output ground is disconnected from the PE.

Ground Continuity Testing

Test Item	Maximum Test Voltage	Test Current	Maximum Resistance	Test Time
PE input and shell	12 V	25 A	0.1 ohm	Until the readings are stable.

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A.3 Reliability

Parameter	Minimum Value	Typical Value	Maximum Value	Unit	Notes & Conditions
Mean time between failures (MTBF)	-	500,000	-	Hours	Telcordia SR332, V_{in} = 230 V AC/ 240 V DC; rated output load, T_A = 25ºC (77°F)

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